

Jan. 21 '05

# S Note-3 Block Diagram

04212-1-Final

## PCB Layer Stackup

- L1:Component
- L2:GND
- L3:Signal 1
- L4:VCC
- L5:Signal 2
- L6:Signal 3
- L7:GND
- L8:Component

## System DC/DC

MAX1977 64

INPUTS	OUTPUTS
VINT16	VCC5M
	VCC3M

## Battery Charger/Selector

MAX1870 58

CV16	M-BAT-PWR
	S-BAT-PWR

## VCC1R8M

MAX1993 67

VINT16	VCC1R8M
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## CPU DC/DC

MAX1907 65

VINT16	VCCCPU/CORE
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## VCC1R05B/VCC1R5M

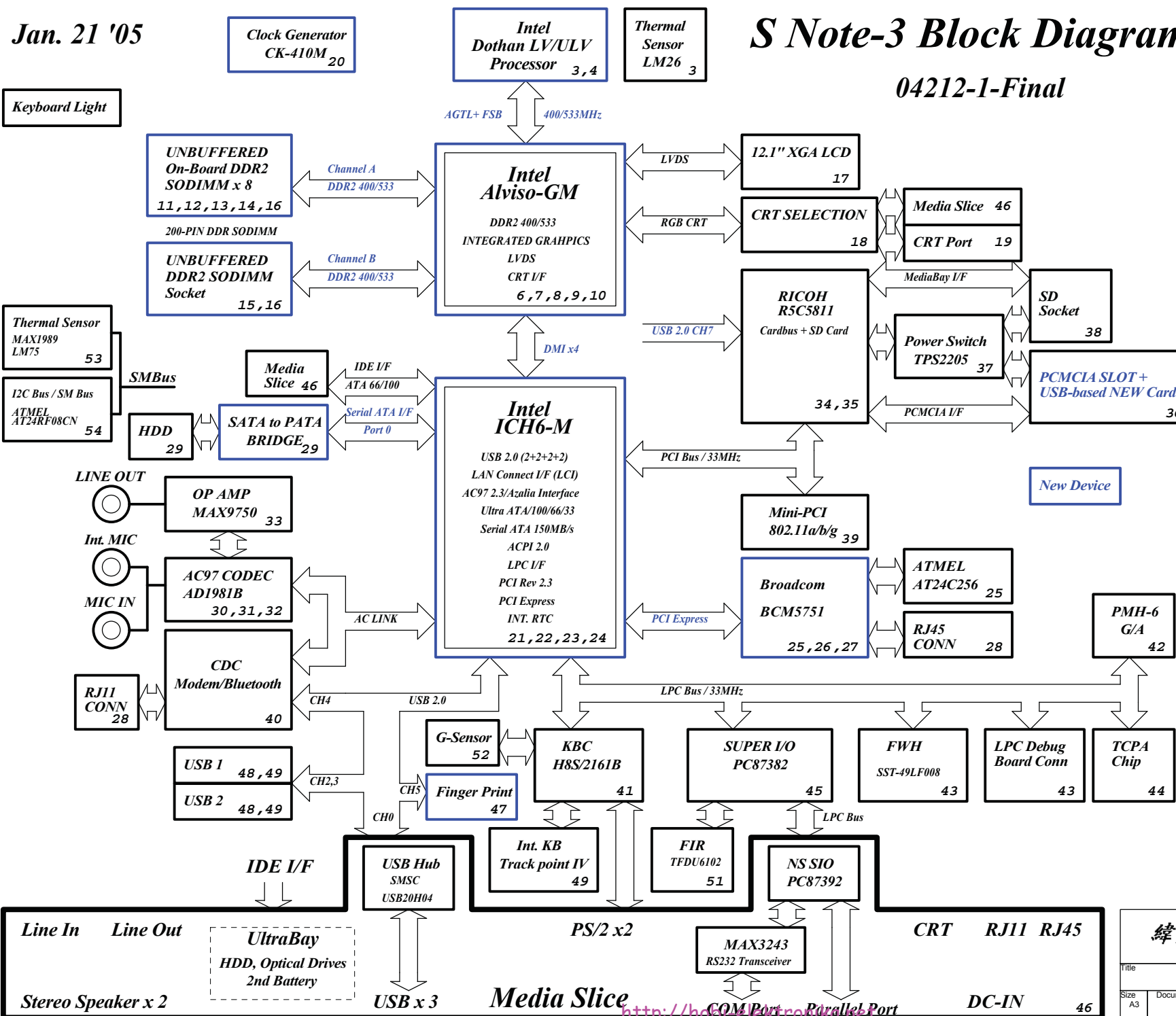
MAX1540 68

VINT16	VCC1R5M
	VCC1R5B

## VCC0R9B/DDR2\_VREF

LP2997MRX 69

VCC1R8A	VCC0R9B
	DDR2_VREF



緯創資通 Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

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RESISTOR

Symbol name	Value	Tolerance (J: 5%, F: 1%, D: 0.5%, B: 0.1 %)	Rating 0402=> 1/16W, 25V 0603 => 1/16W, 75V 0805 => 1/10W, 100V	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
10KR3	10K Ohm	If no letter, it means J: 5%	1/16W, 75V	0603
33D3R5	33.3 Ohm	If no letter, it means J: 5%	1/10W, 100V	0805
1KR3F	1K Ohm	F: 1%	1/16W, 75V	0603

The naming rule is value + R + size + tolerance  
For the value, it can be read by the number before R. (R means resistor)  
For the tolerance, it can be read from the last letter.  
For the rating, we don't show on the symbol name.  
For the size, R2=>0402, R3=>0603, R5=>0805,....

CAPACITOR

Symbol name	Value	Tolerance (M: +/-20, K: +/-10, Z: +80/-20)	Rating	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
SCD1U10V2MX-1	0.1uF	M/X5R	10V	0402
SC10U6D3V5MX	10uF	M/X5R	6.3V	0805
SC2D2U16V5ZY	2.2uF	Z/Y5V	16V	0805

The naming rule is  
Capacitor type + value + rating + size + tolerance + material  
SCD1U10V2MX-1  
SC=> SMT Ceramic, TC=> POS cap or SP cap  
D1U => 0.1uF  
10V => the voltage rating is 10V  
2=> 0402, 3=>0603, 5=>0805  
M=>tolerance M, K, Z  
X=> X7R/X5R, Y=> Y5V  
-1 => symbol version, nonsense to EE characteristic

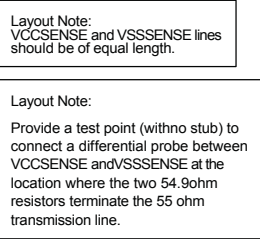
PLANAR\_ID[3..0]

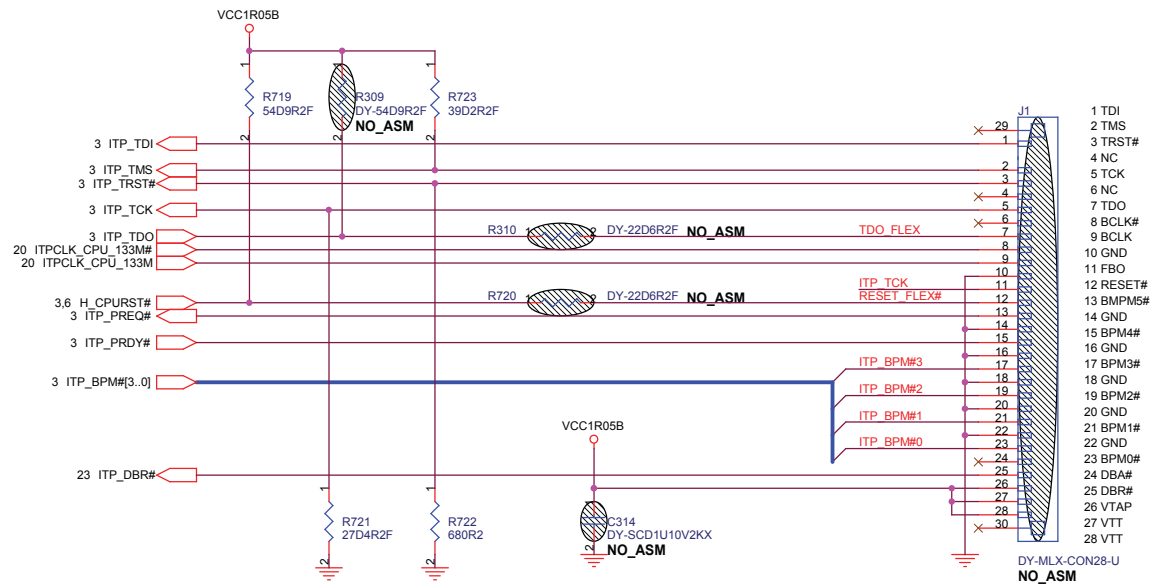
ICH4-M GPiOn	31	30	29	26	Planar ID Version	Planar PCB Version
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	0	0	0	0	SDV	04212-SA
	0	0	0	1	SIV	04212-SB
	0	0	1	0	SIT	04212-SC
	0	0	1	1	SVT	04212-1
	0	1	0	0		
	0	1	0	1		
	0	1	1	0		
	0	1	1	1		

PCI TABLE

DEVICE	IDSEL	IRQ (Default)	REQ# / GNT#
MINIPCI SLOT	AD18	F, G	REQ# 3/ GNT#3
CARDBUS R5C811	AD16	SERIRQ	REQ#0 / GNT#0
USB UHCI	AD29	A, C, D	
USB 2.0 EHCI	AD29	H	
DMI-to-PCI/ AC97 Modem/ AC97 Audio	AD30	B B	
LPC Bridge IDE SATA SMBus	AD31	C C B	
PCI Express	AD28	A, B, C, D	



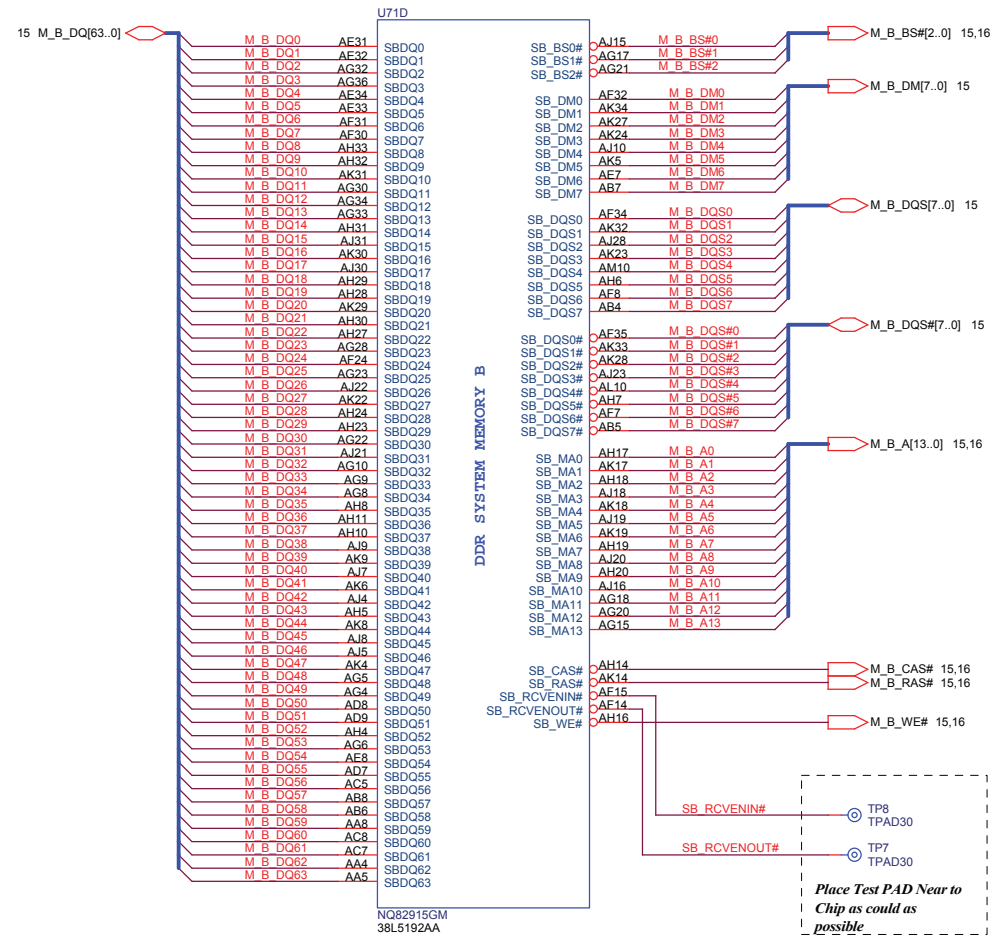
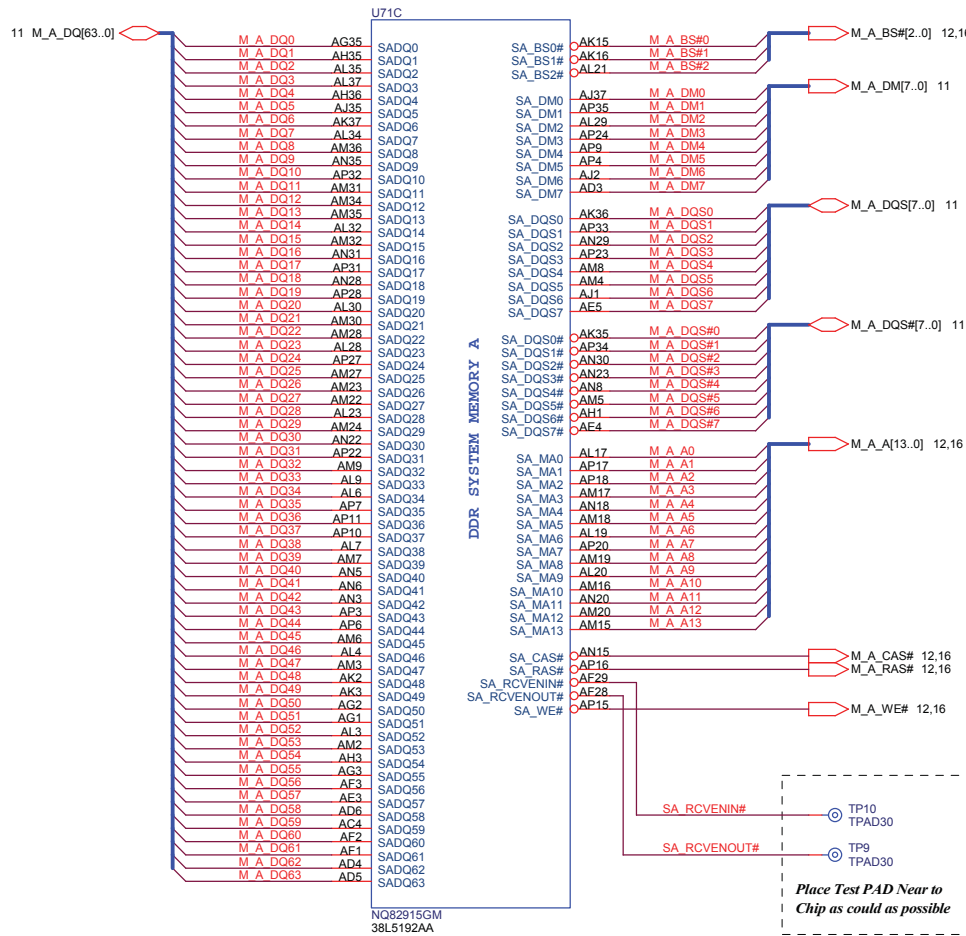




(\*1) TCK SIGNAL IS BRANCHED AT DOTHAN PIN

(\*2) CPURST# SIGNAL IS BRANCHED AT ALVISO PIN

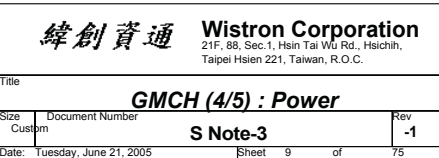










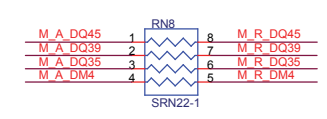
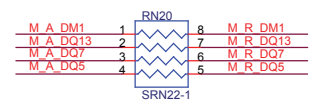
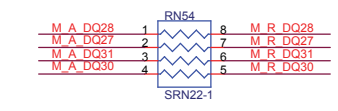
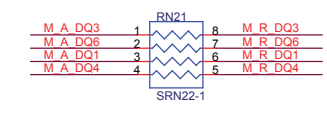
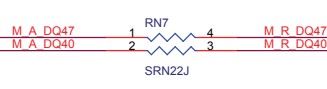
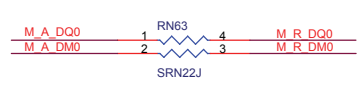
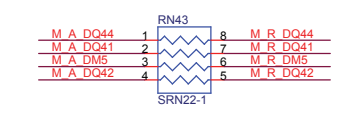
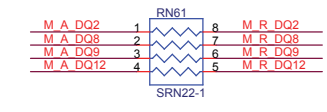
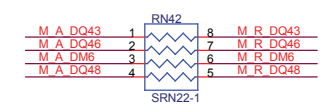
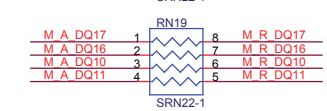
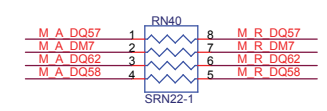
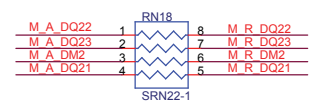
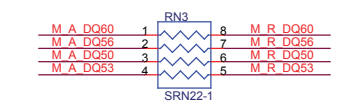
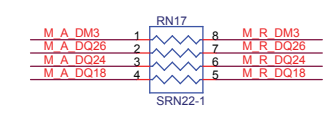
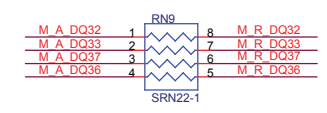
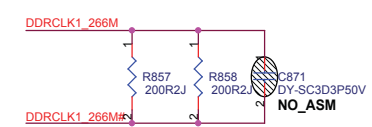
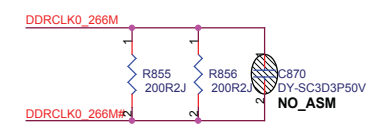
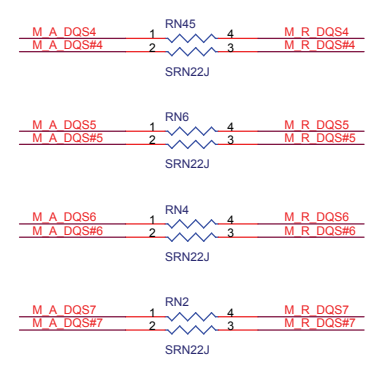
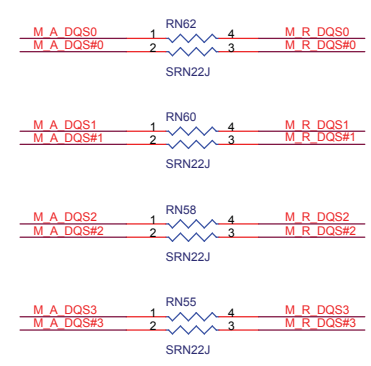
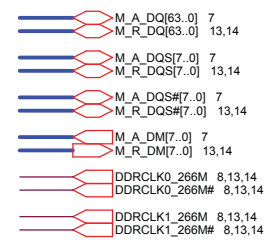


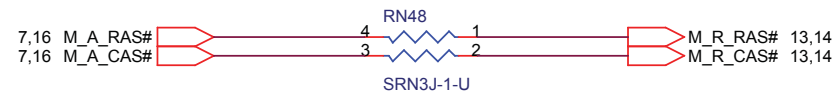
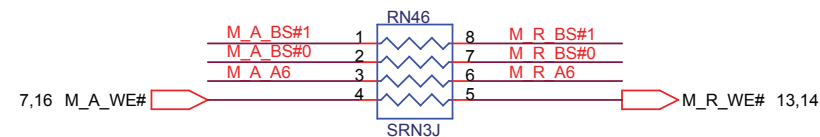
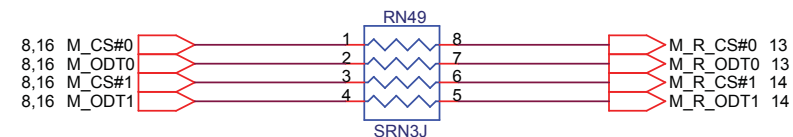
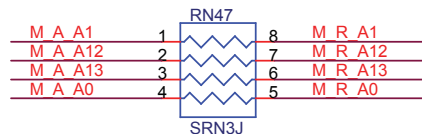
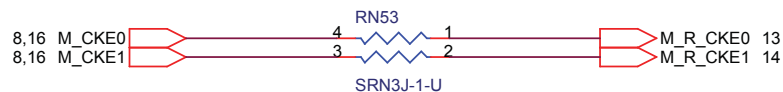
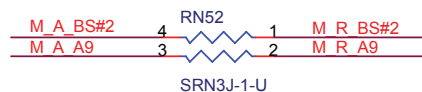
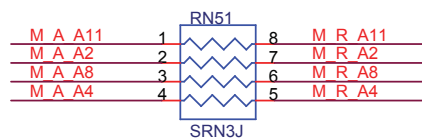
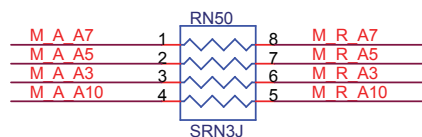
U71F  
NQ82915GM  
38L5192AA



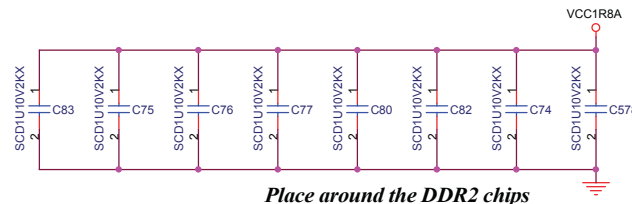
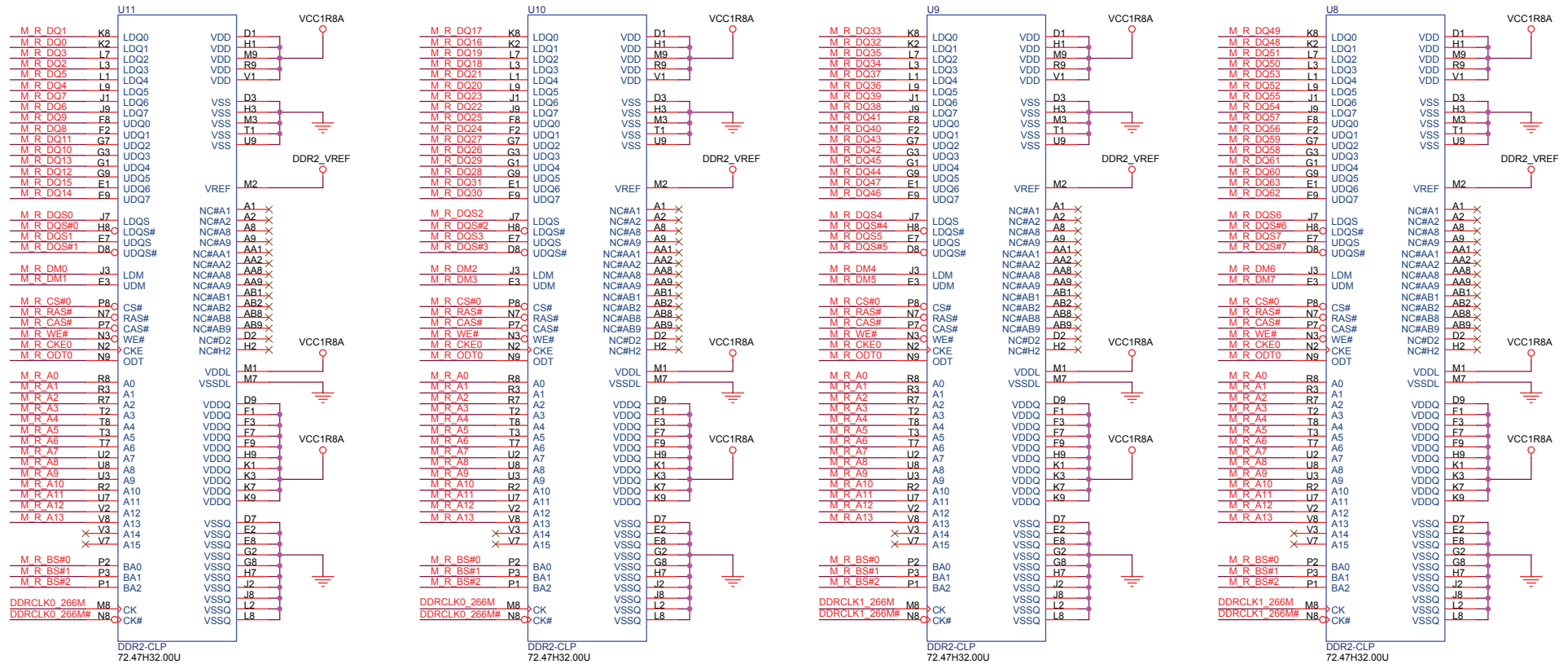
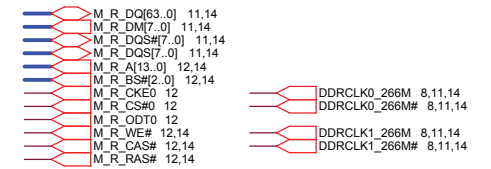
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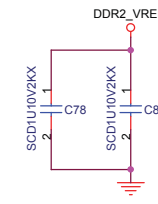




# On-board DDR2 Memory



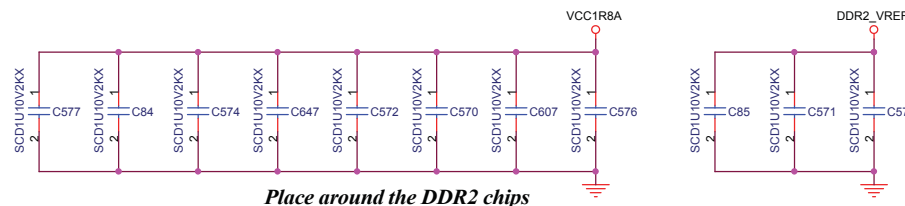
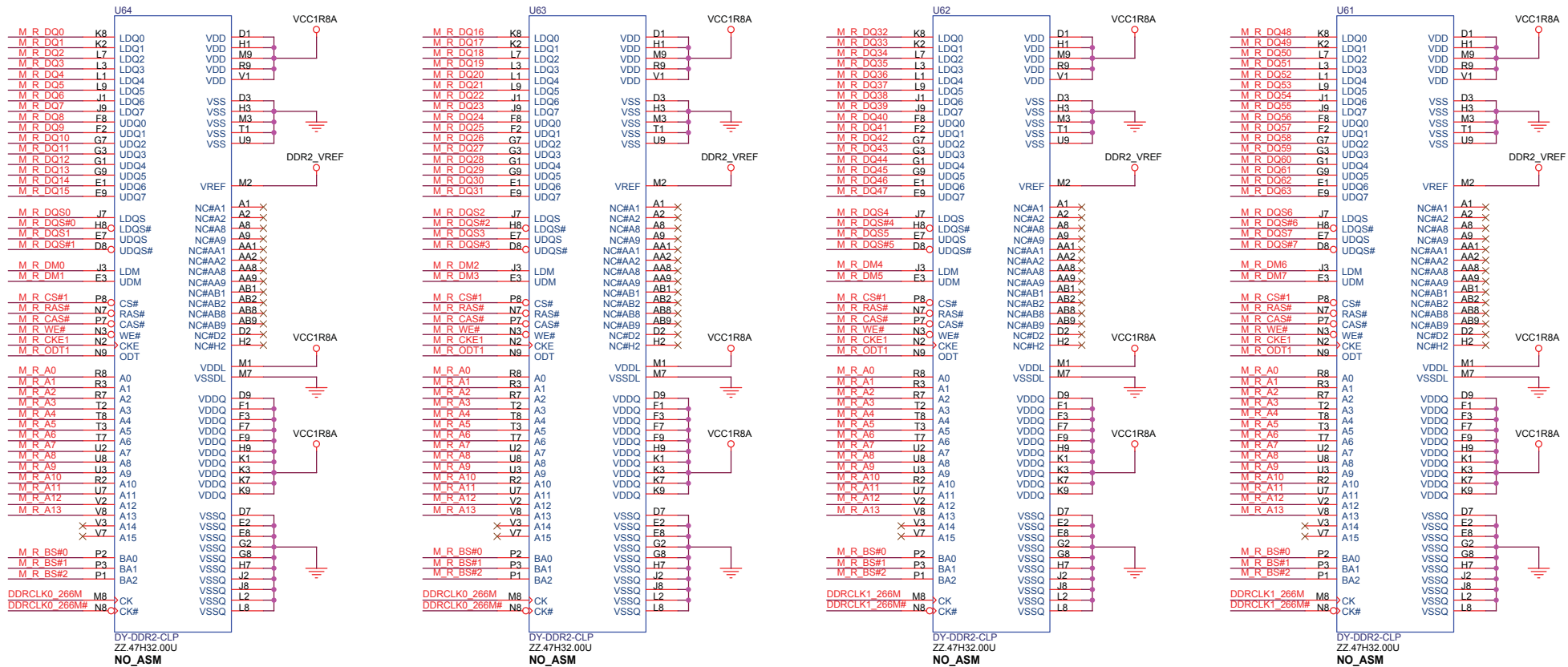
Place around the DDR2 chips



<b>緯創資通 Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title: <b>BASE MEMORY BANK0</b>	
Size: A3	Document Number: <b>S Note-3</b>
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# On-board DDR2 Memory

M_R_DQ[63..0]	11,13	
M_R_DM[7..0]	11,13	
M_R_DQS#[7..0]	11,13	
M_R_DQS[7..0]	11,13	
M_R_A[13..0]	12,13	
M_R_BS#[2..0]	12,13	
M_R_CKE1	12	
M_R_CS#1	12	
M_R_ODT1	12	
M_R_WE#	12,13	
M_R_CAS#	12,13	
M_R_RAS#	12,13	
DDRCLK0_266M	8,11,13	
DDRCLK0_266M#	8,11,13	
DDRCLK1_266M	8,11,13	
DDRCLK1_266M#	8,11,13	

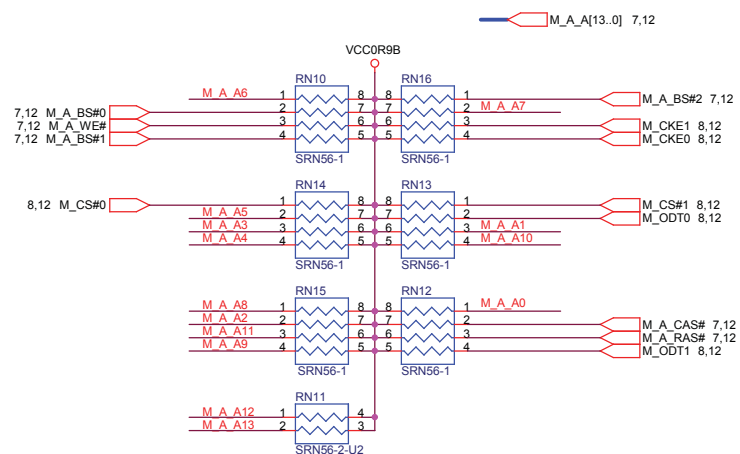


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Title	
<b>BASE MEMORY BANK1</b>	
Size	Document Number
A3	
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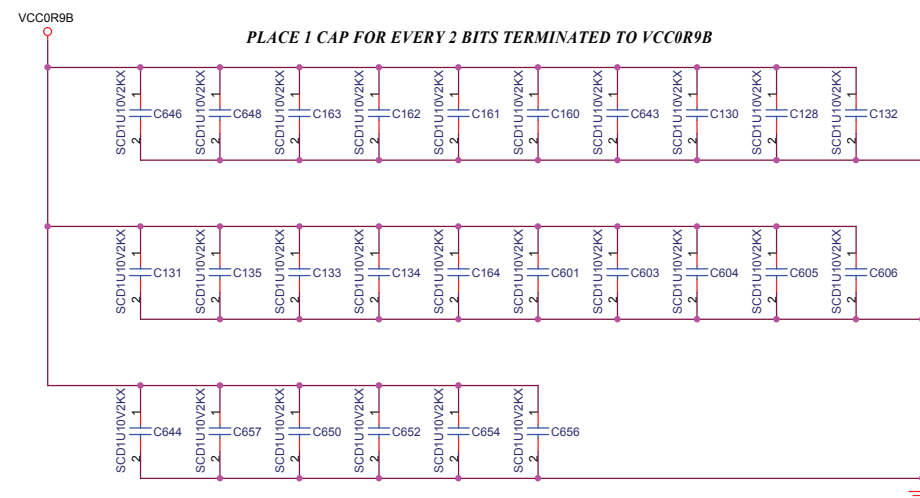
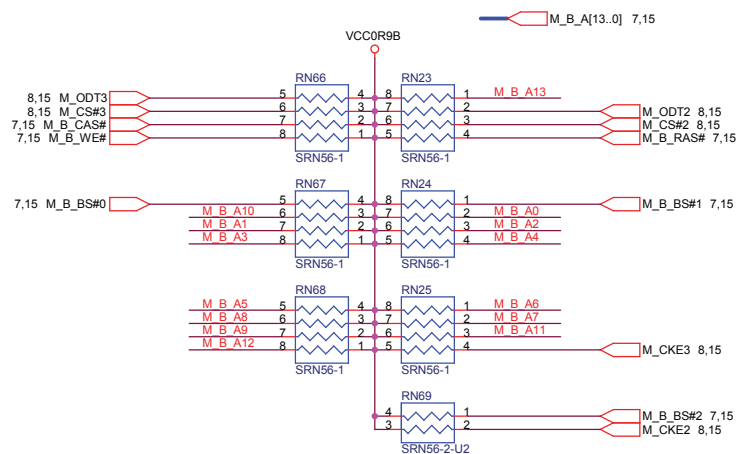




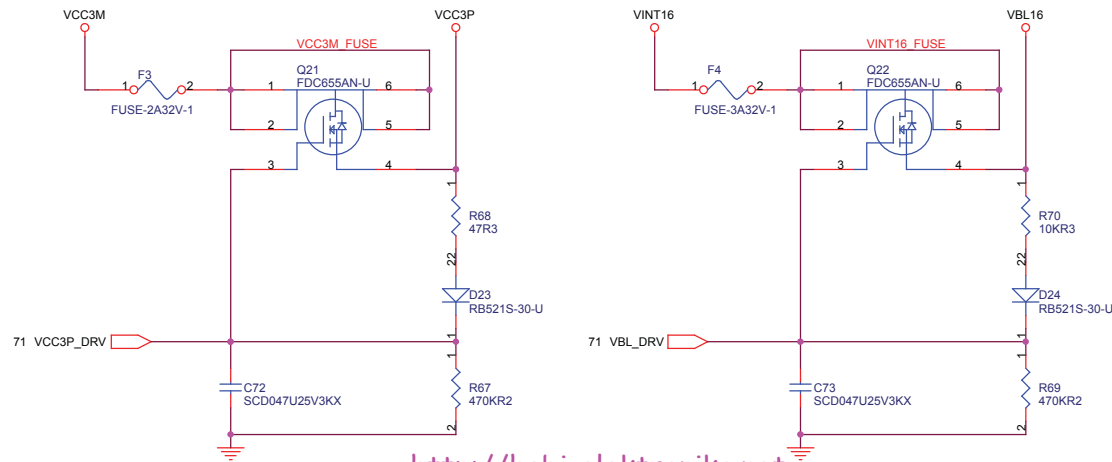
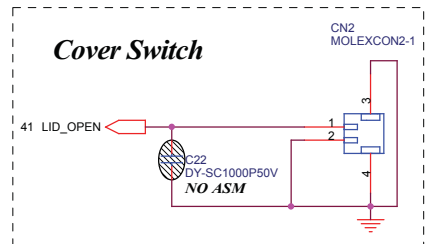
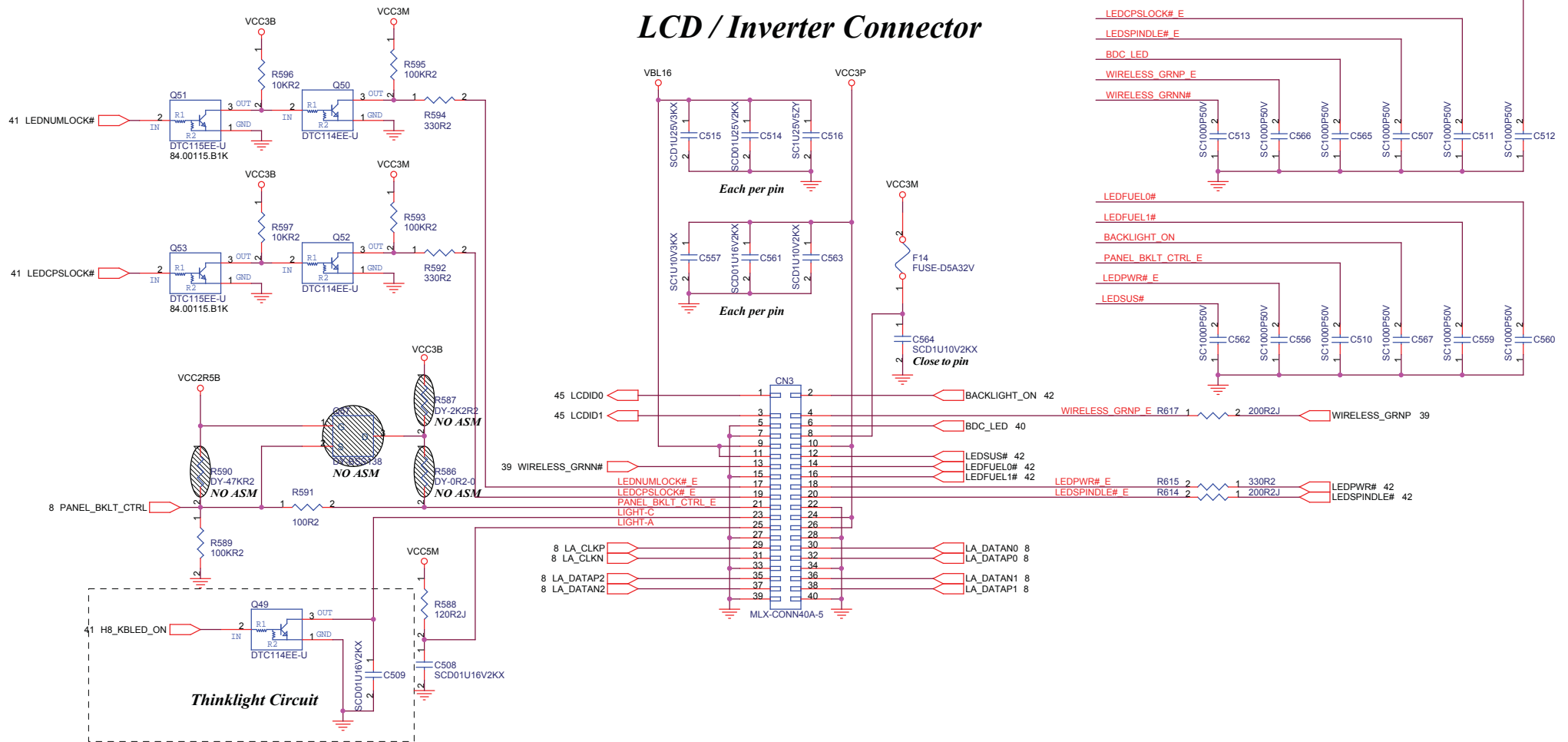
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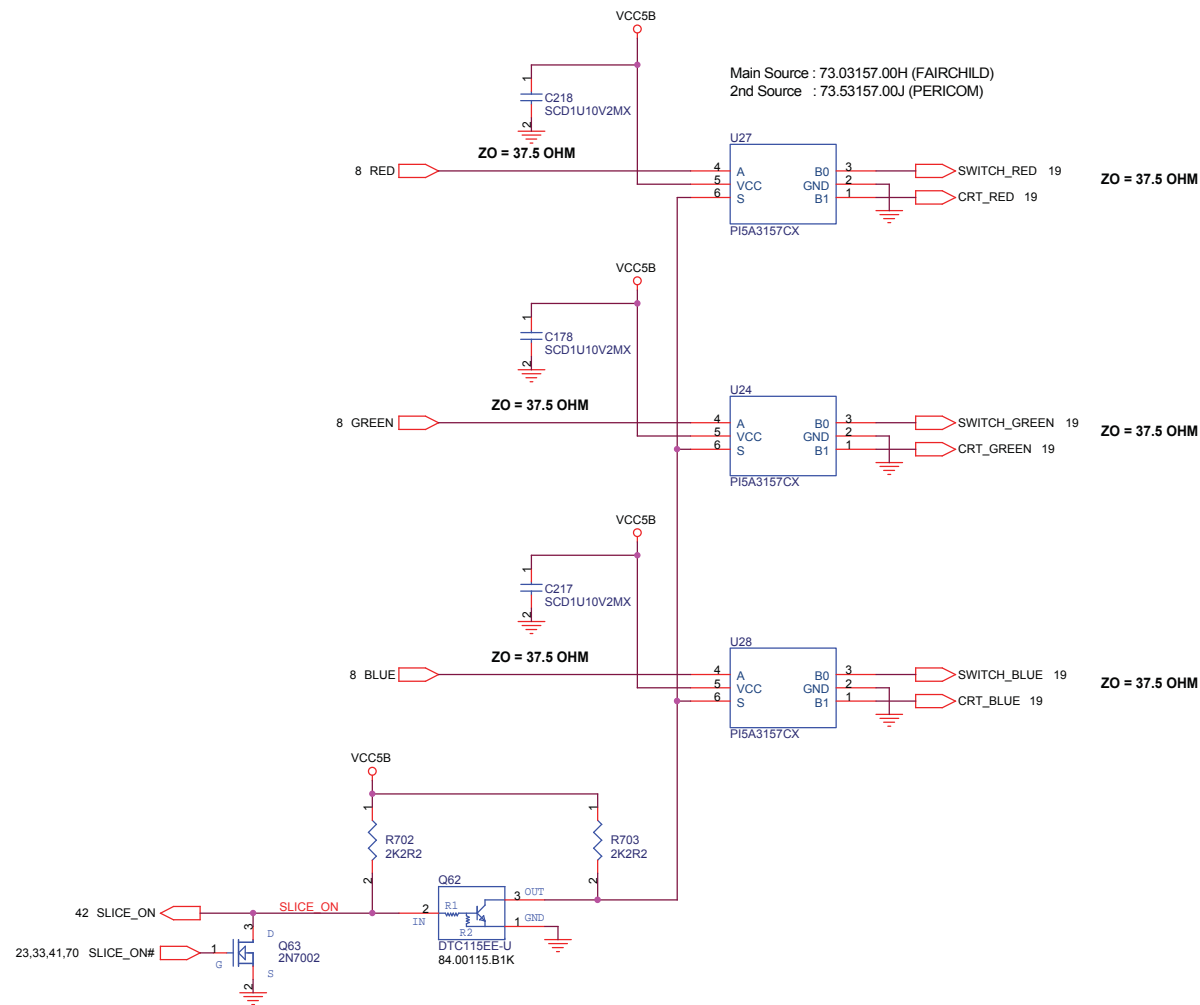
### CHANNEL B PARALLEL TERMINATION

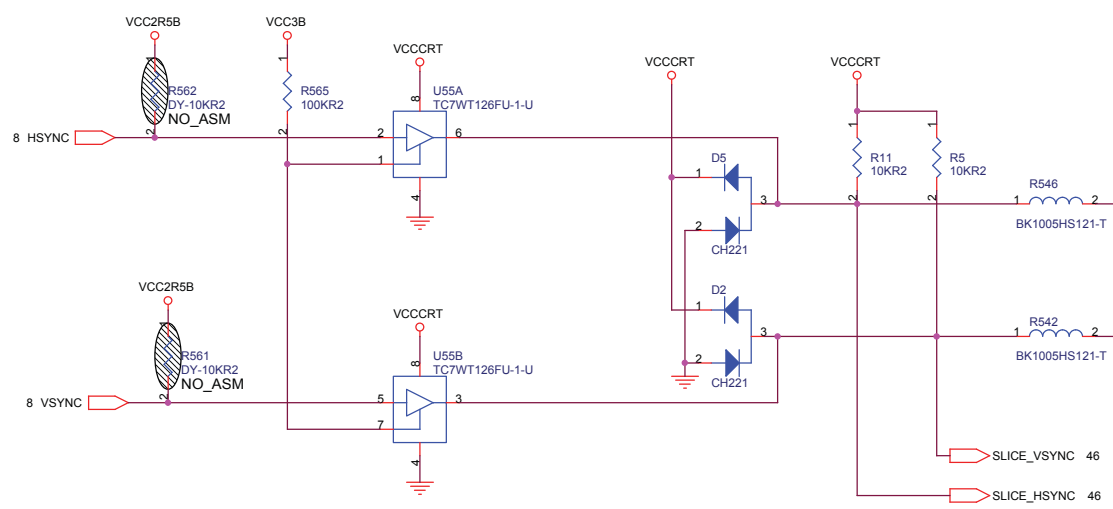
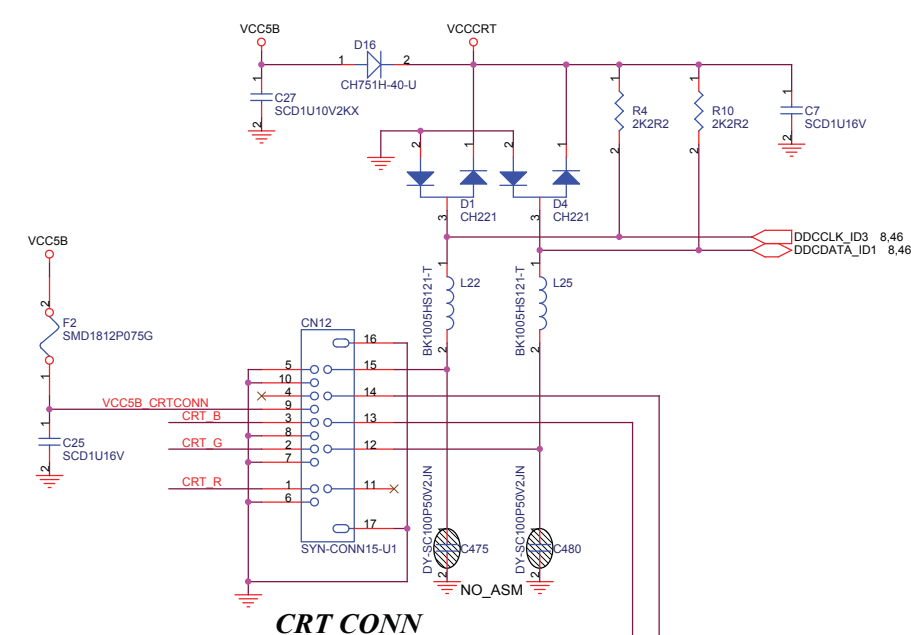
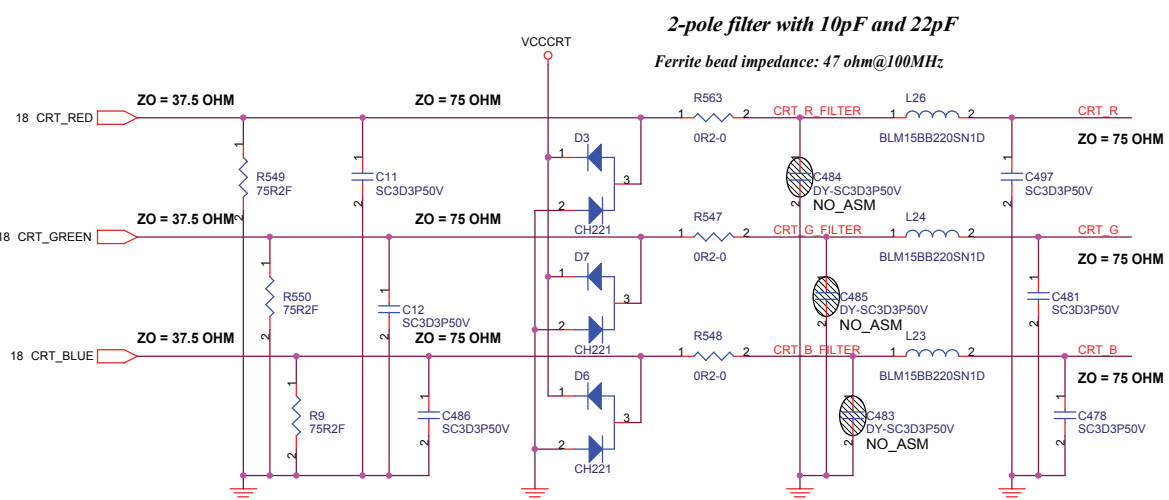
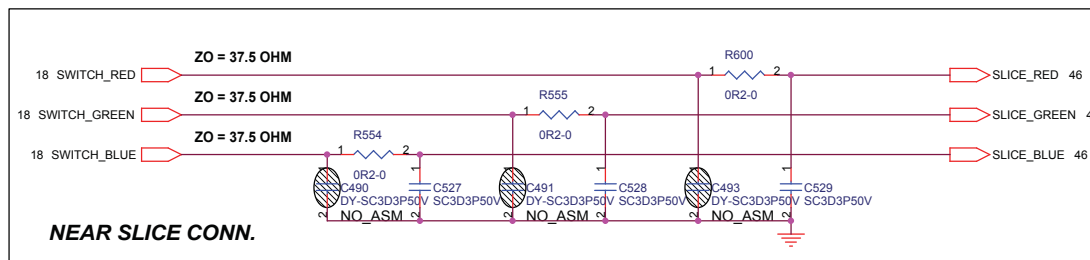


# LCD / Inverter Connector



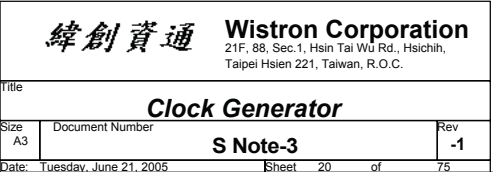
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<b>LCD CONNECTOR</b>	
Title Size A3 Date: Tuesday June 21, 2005	Document Number <b>S Note-3</b> Sheet 17 of 75
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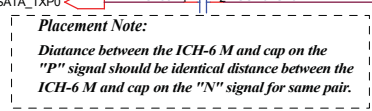
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Title	<b>EXT CRT CONNECTOR</b>
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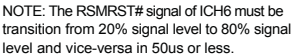
**RTC Circuitry**

The diagram shows a circuit for the Real Time Clock (RTC). A resistor labeled R369 (100K $\Omega$ ) is connected between the VCC3B supply and the CN7 MLX-CON3-3 connector. The connector has pins labeled 1, 2, 3, and 4. Pin 1 is connected to ground, and pin 4 is connected to VCC3B. Pins 2 and 3 are connected to the RTC circuitry.

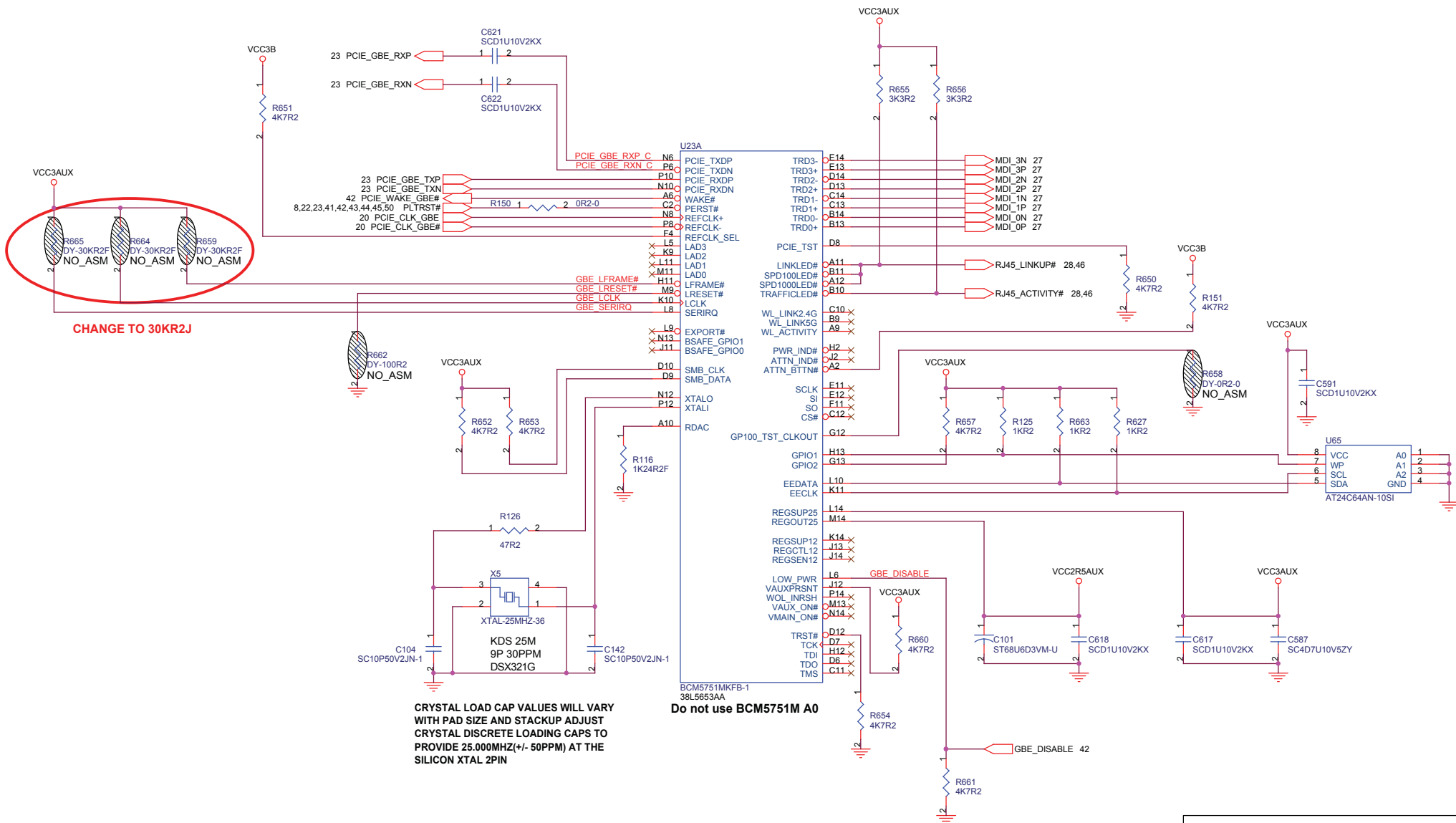


 <b>緯創資通</b>		<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
<b>ICH6-M (1/4)</b>			
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<b>S Note-3</b>			

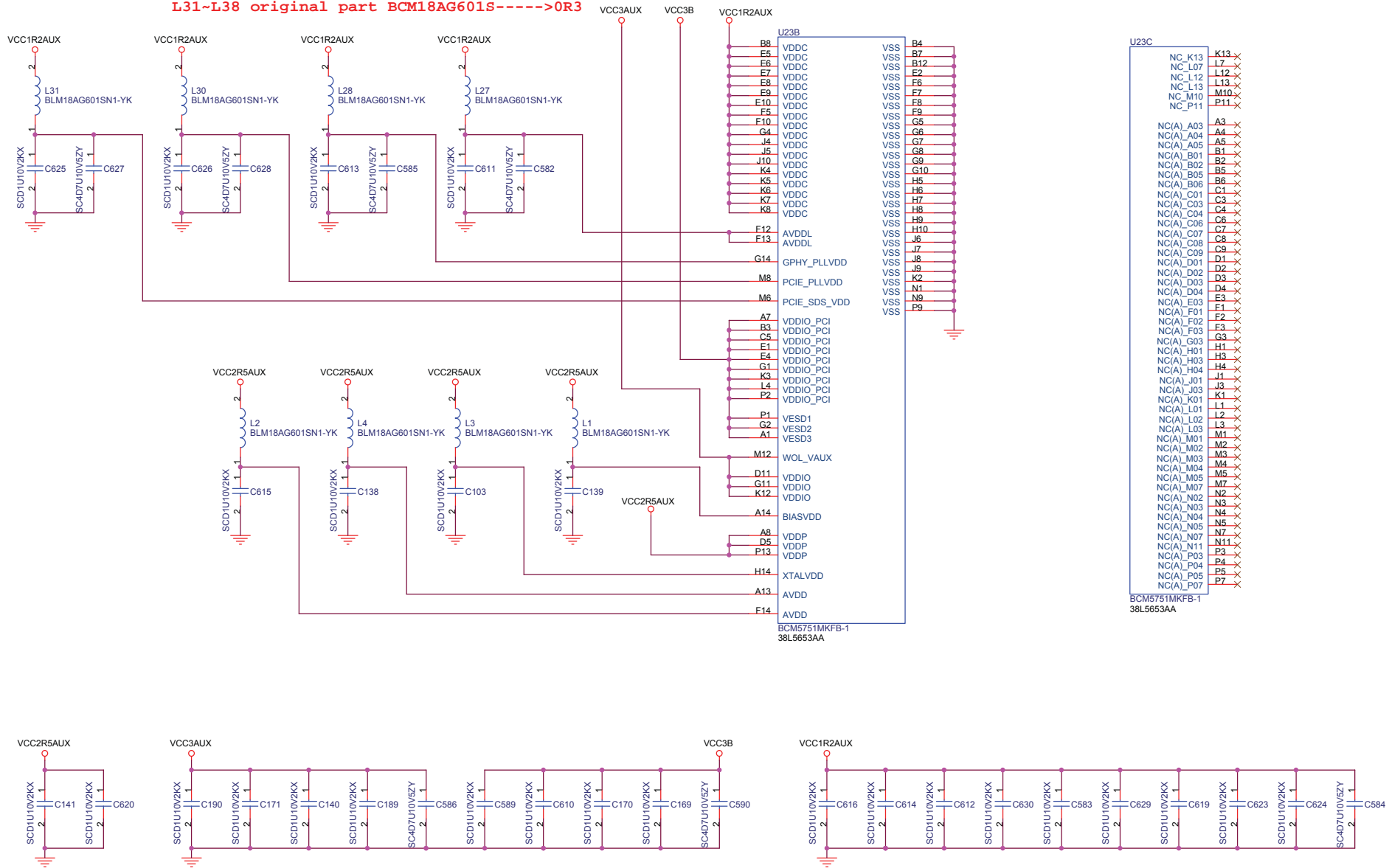




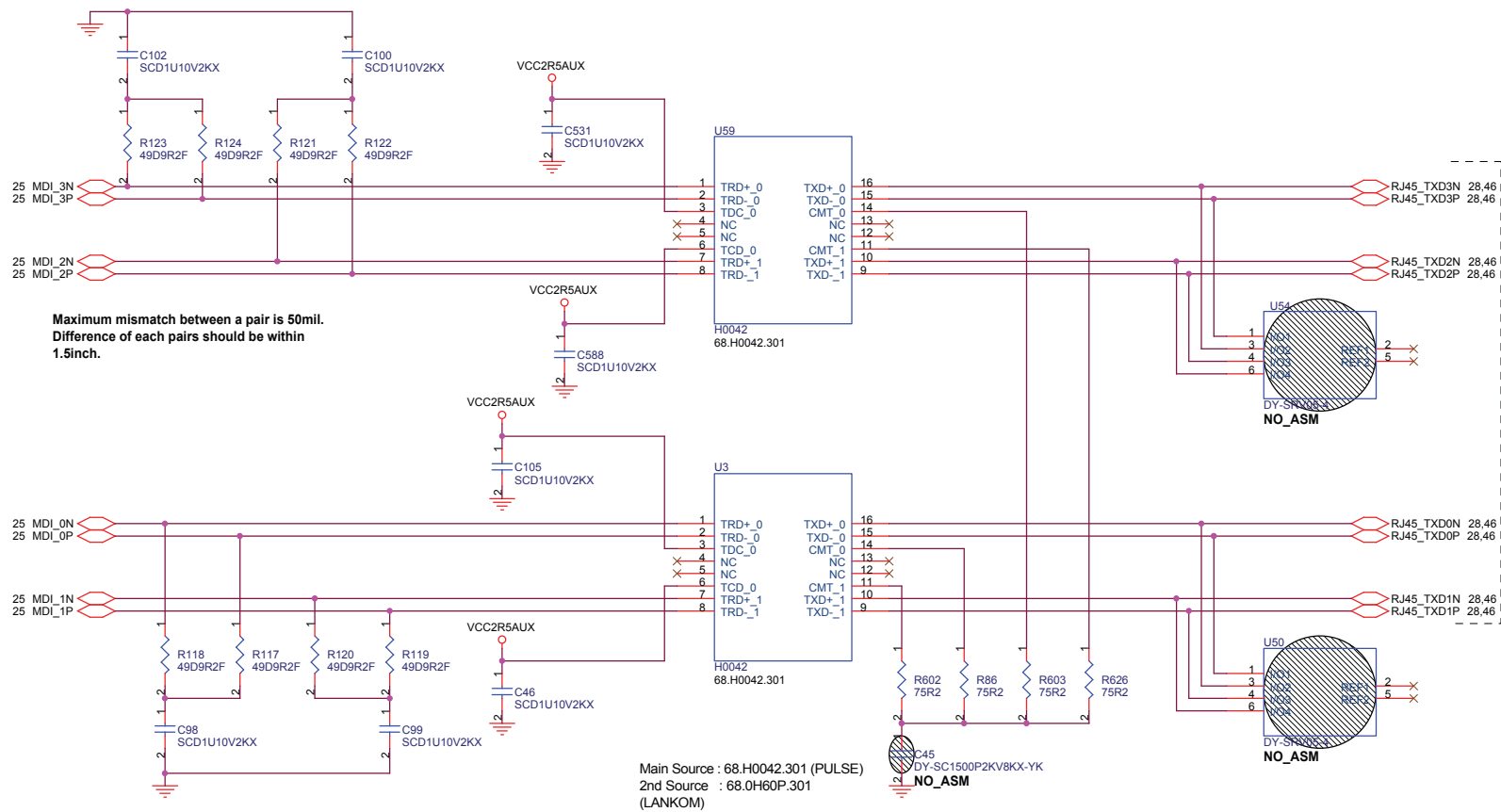




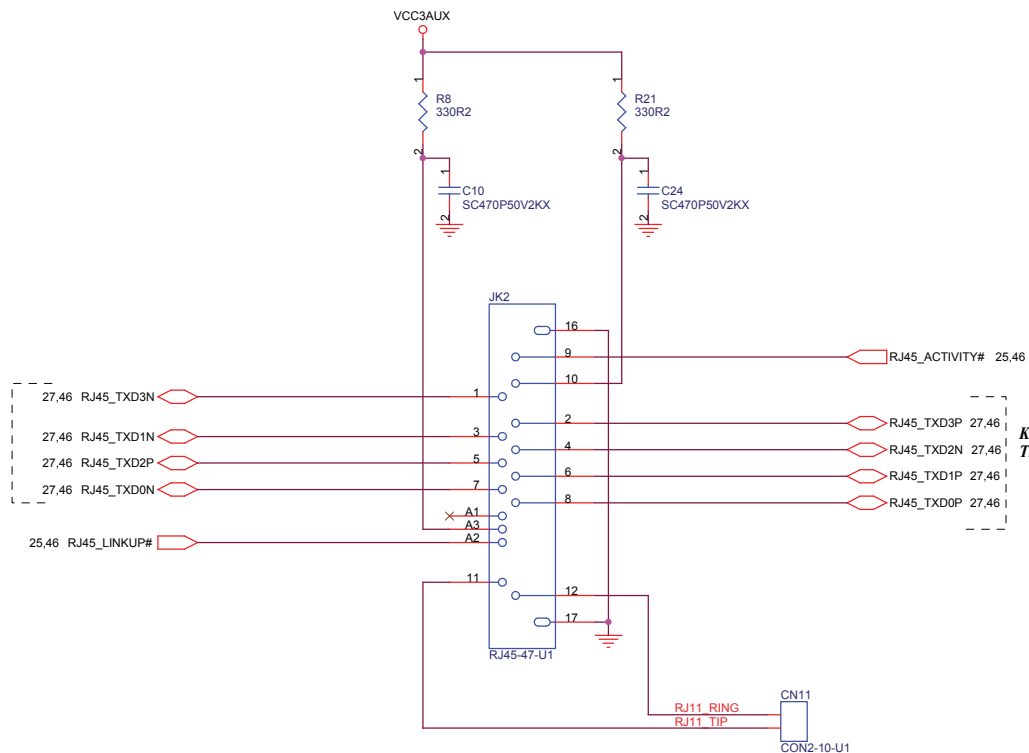
L31~L38 original part BCM18AG601S----->0R3







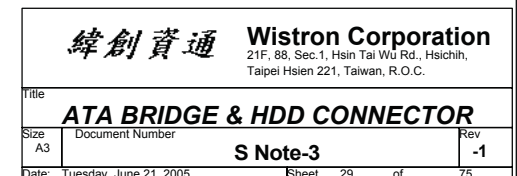
KEEP AWAY FROM OTHER  
TRACES



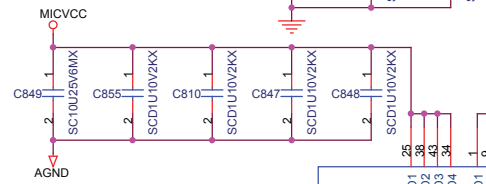
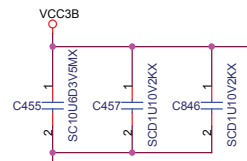
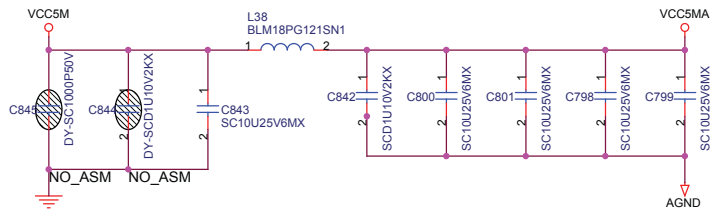
KEEP AWAY FROM OTHER  
TRACES

S-ATA TABLE 2/2

U42	88SA8040	Si3811
R430	NO_ASM	NO_ASM
R424	ASM	NO_ASM
R482	NO_ASM	ASM
R486	12_TK	TK
R416	NO_ASM	NO_ASM
R423	NO_ASM	NO_ASM
R782	ASM	ASM
R803	ASM	NO_ASM
R804	NO_ASM	NO_ASM
R483	NO_ASM	ASM
R484	NO_ASM	ASM
R802	NO_ASM	NO_ASM
R414	NO_ASM	ASM
R489	NO_ASM	ASM

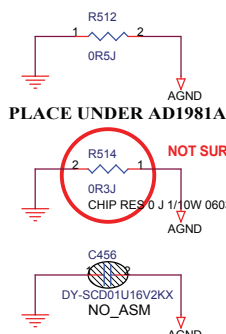
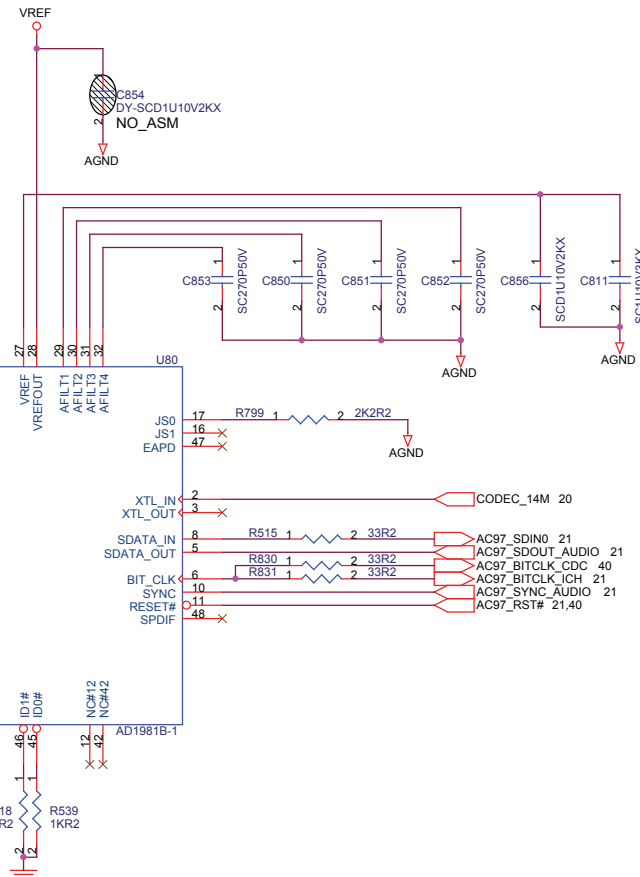
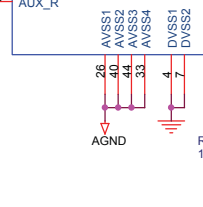


# VCC5MA



31 MIC\_M  
33 L\_OUT  
33 R\_OUT  
32 CD\_AUDIO\_L  
32 CD\_AUDIO\_R  
32 CD\_AUDIO\_GND

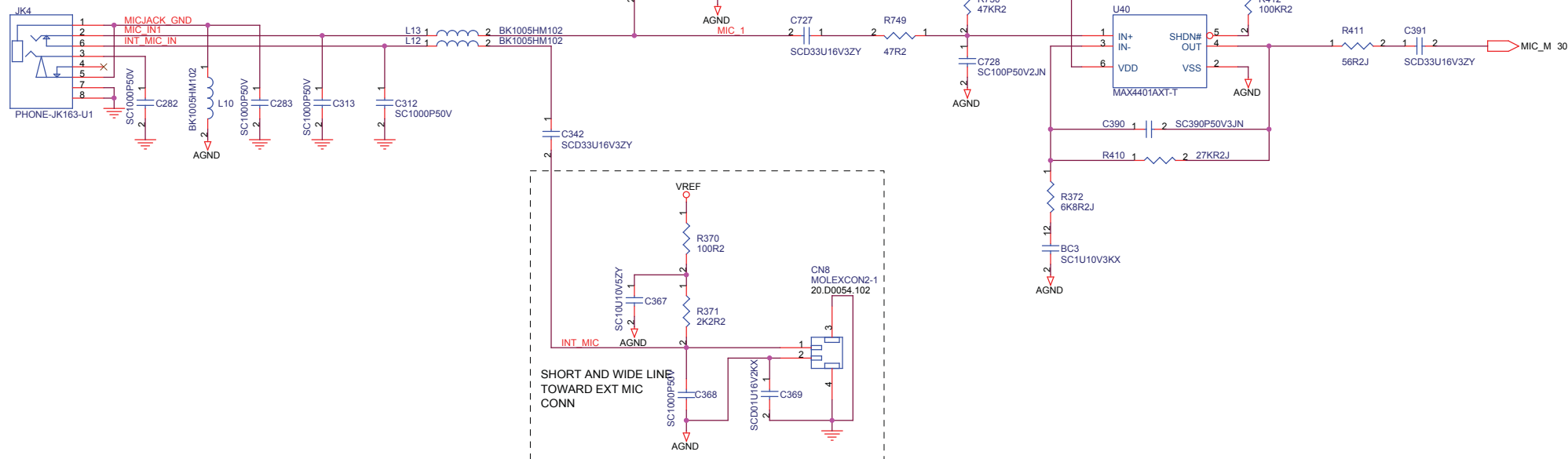
21 MIC1  
22 MIC2  
23 LINE\_IN\_L  
24 LINE\_IN\_R  
25 LINE\_OUT\_L  
26 LINE\_OUT\_R  
27 MONO\_OUT  
28 PHONE\_IN  
18 CD\_L  
20 CD\_R  
19 CD\_GND\_REF  
39 HP\_OUT\_L  
41 HP\_OUT\_R  
14 AUX\_L  
15 AUX\_R



<http://hobi-elektronika.net>

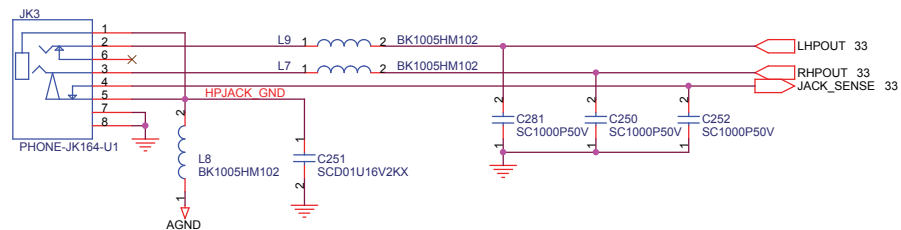
<b>緯創資通 Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title: <b>AUDIO 1981B</b>	
Size: A3	Document Number: <b>S Note-3</b>
Date: Tuesday, June 21, 2005	Sheet: 30 of 75
Rev: -1	

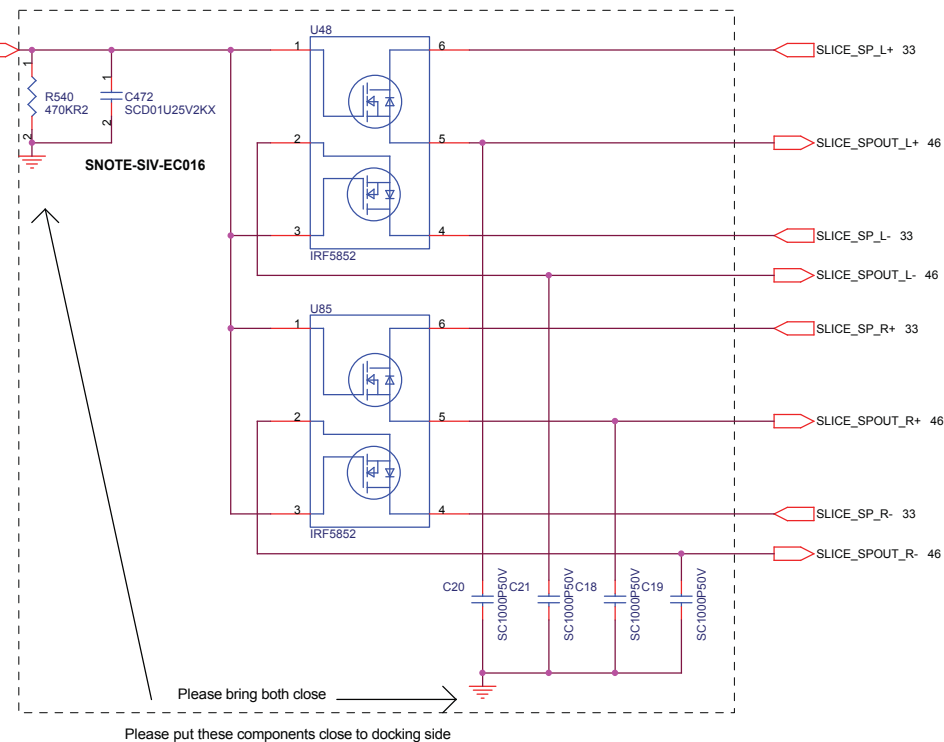
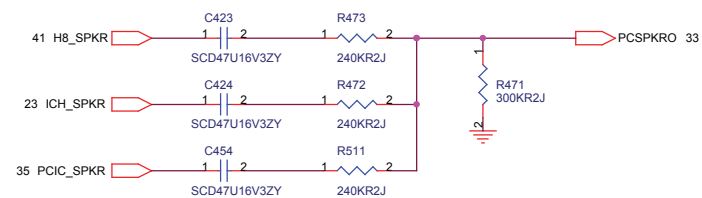
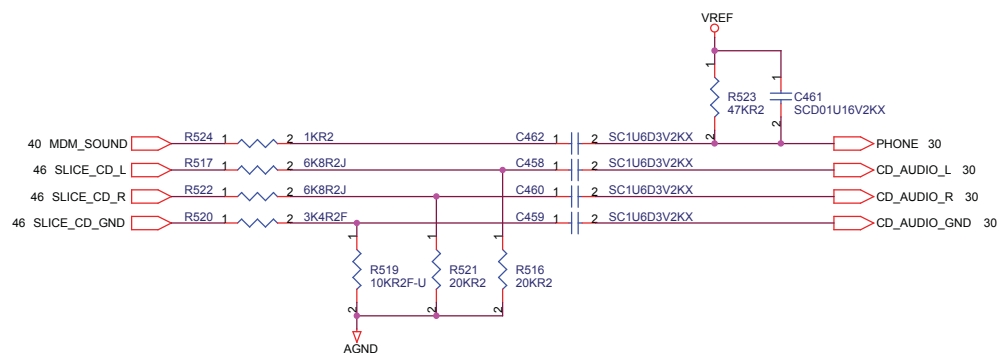
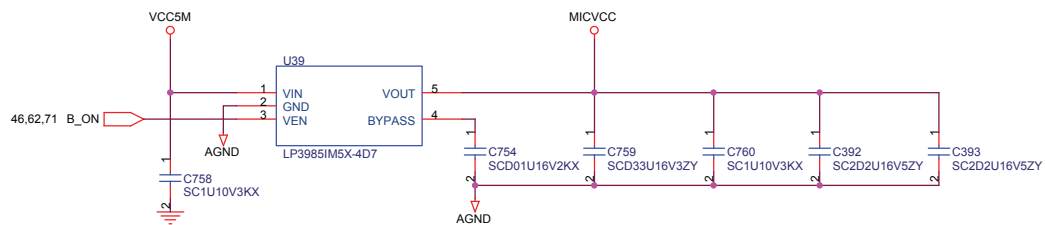
## EXT MIC CONN



ALL PARTS MUST BE PLACED NEAR TO INTERNAL MICROPHONE

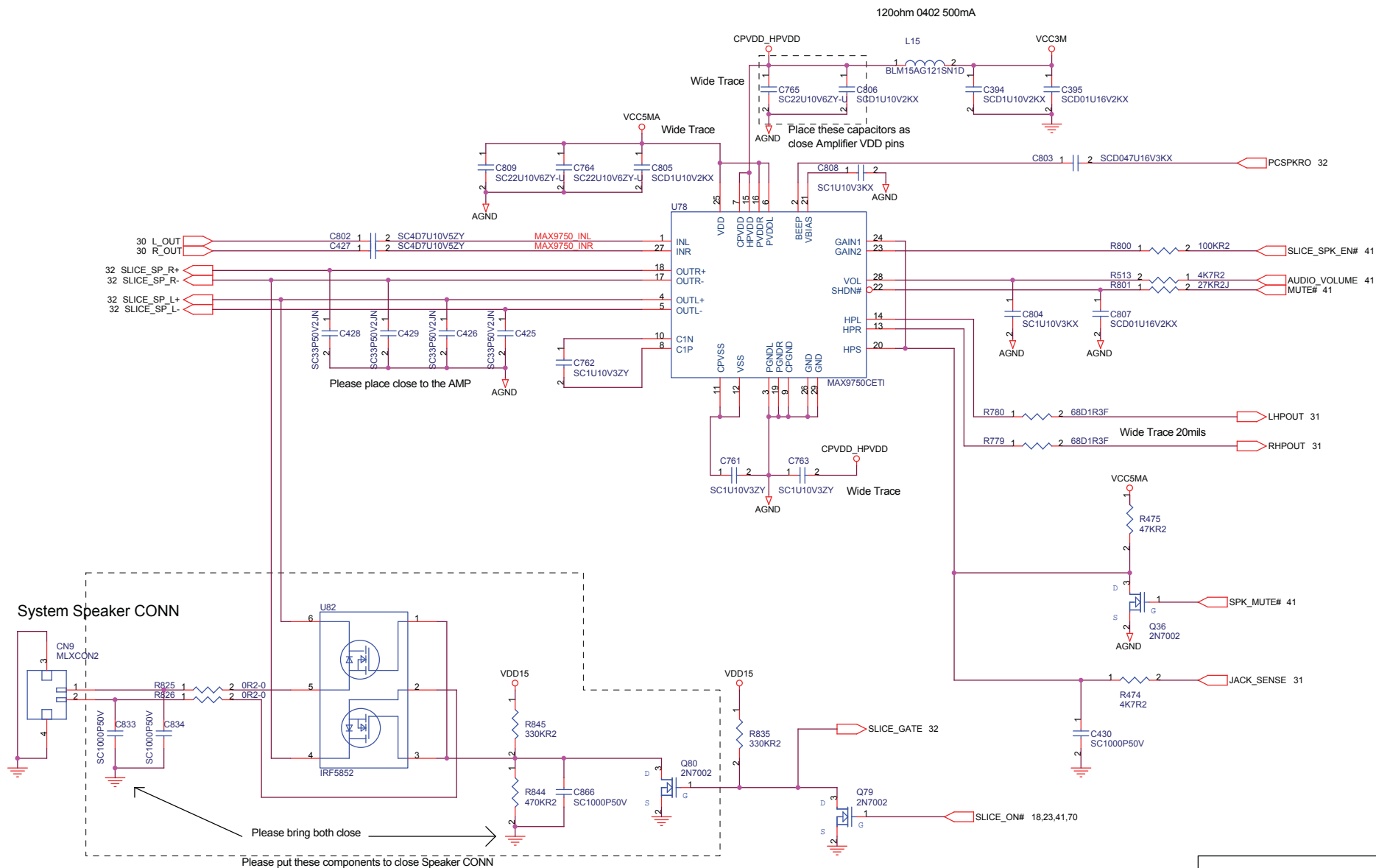
## HEADPHONE CONN



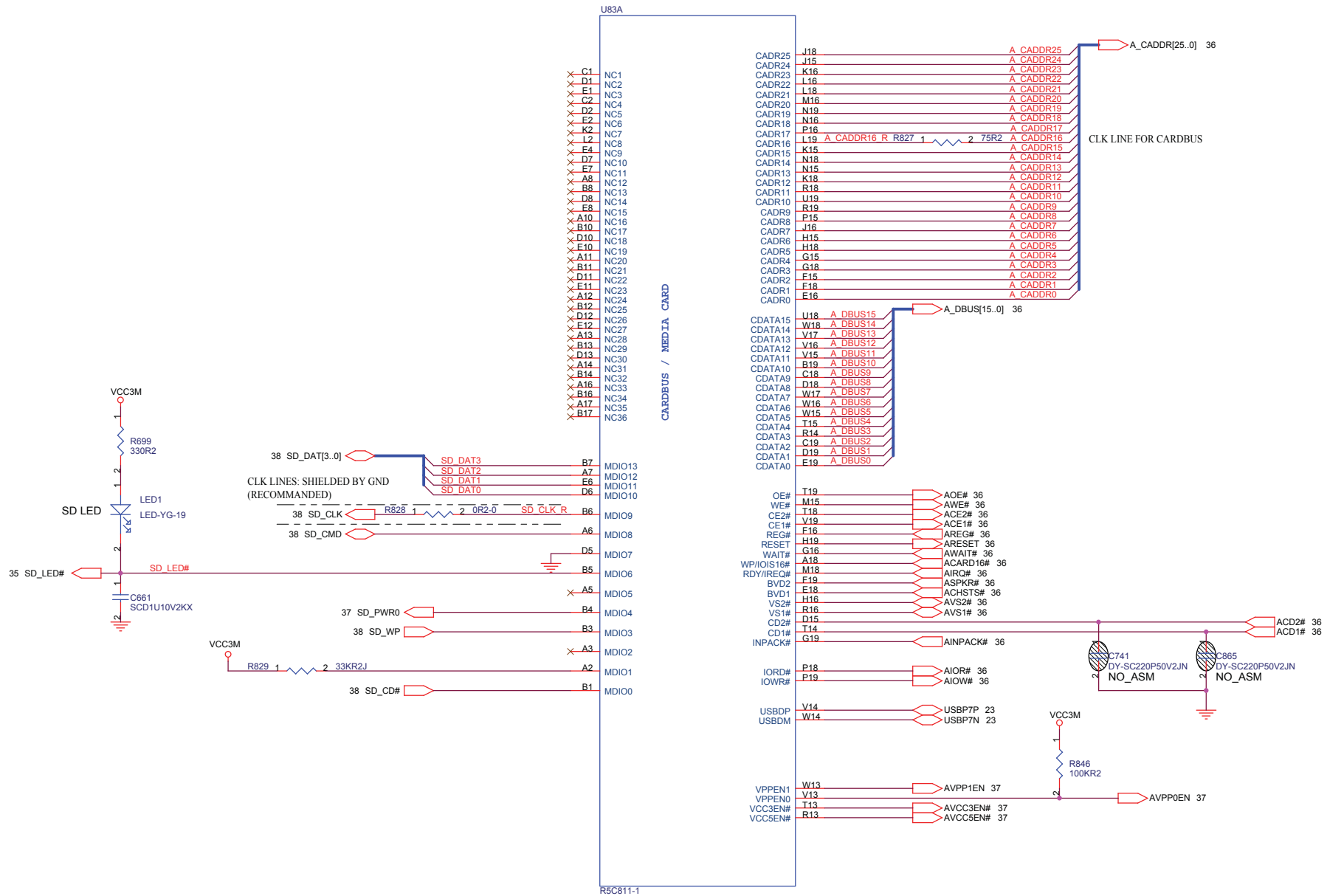


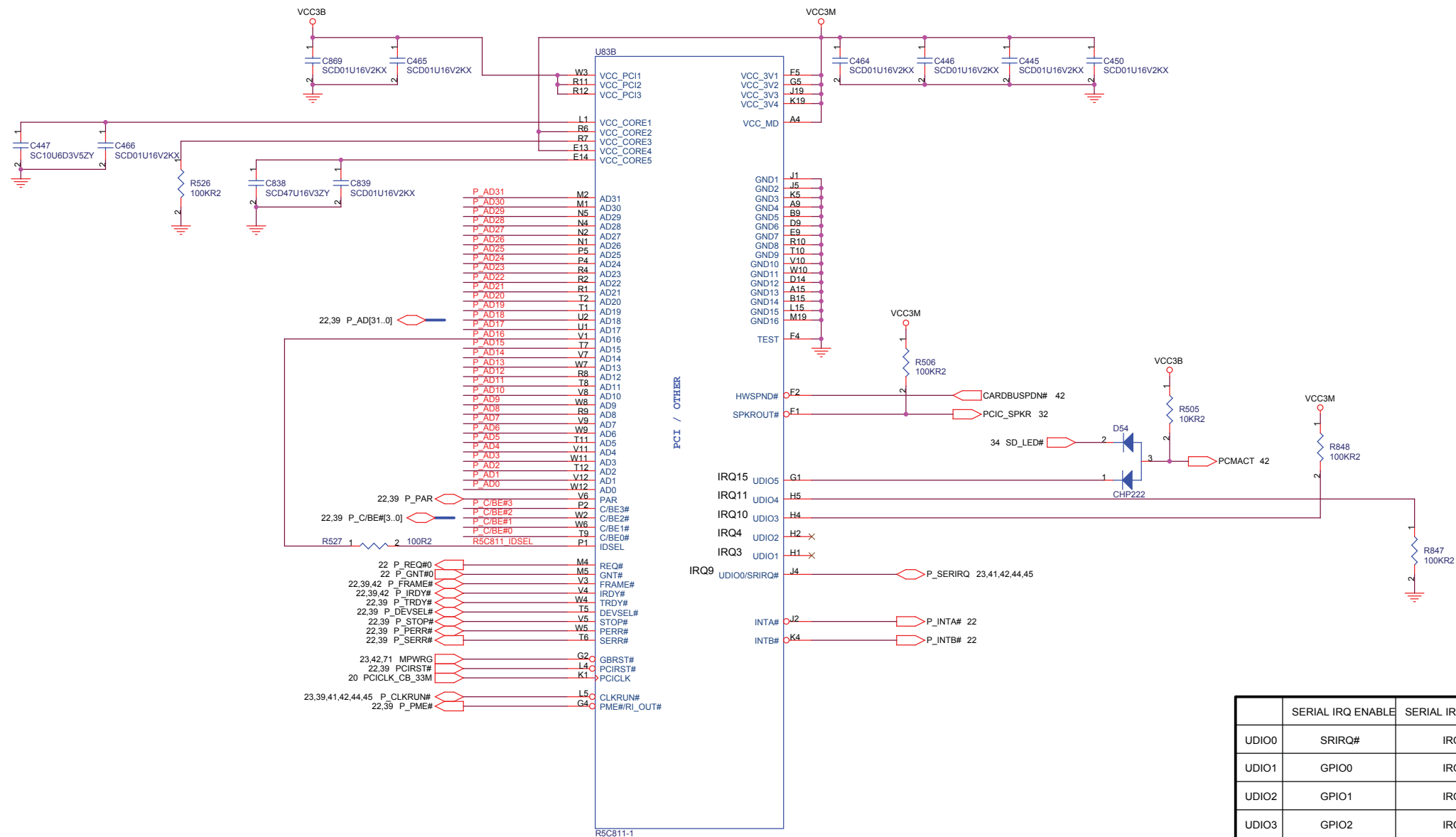


SPEAKER MODE GAIN (dB)				HEADPHONE MODE GAIN (dB)	
GAIN1 = 0 GAIN2 = 0	GAIN1 = 1 GAIN2 = 0	GAIN1 = 0 GAIN2 = 1	GAIN1 = 1 GAIN2 = 1	GAIN1 = X GAIN2 = 0	GAIN1 = X GAIN2 = 1
6	7.5	9	10.5	0	3



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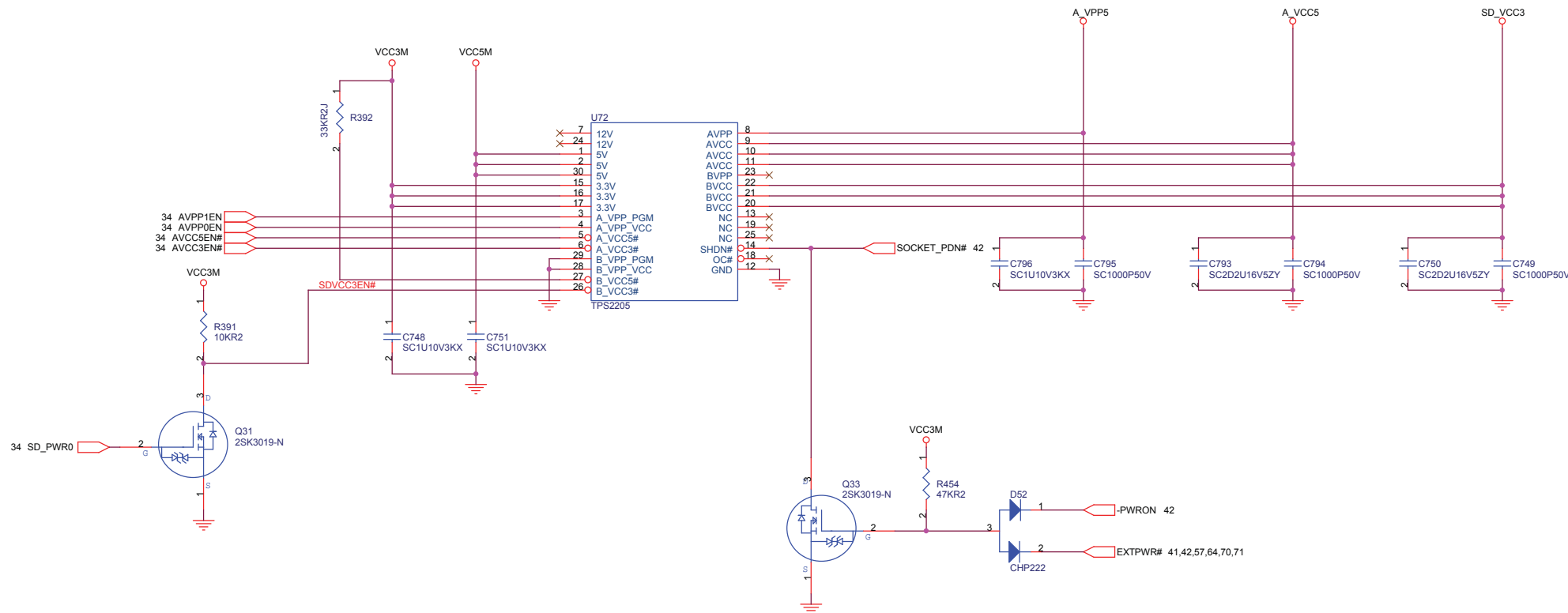


	SERIAL IRQ ENABLE	SERIAL IRQ DISABLE
UDIO0	SRIRQ#	IRQ9
UDIO1	GPIO0	IRQ3
UDIO2	GPIO1	IRQ4
UDIO3	GPIO2	IRQ10
UDIO4	GPIO3	IRQ11
UDIO5	LED0#	IRQ15

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Taipei Hsien 221, Taiwan, R.O.C.

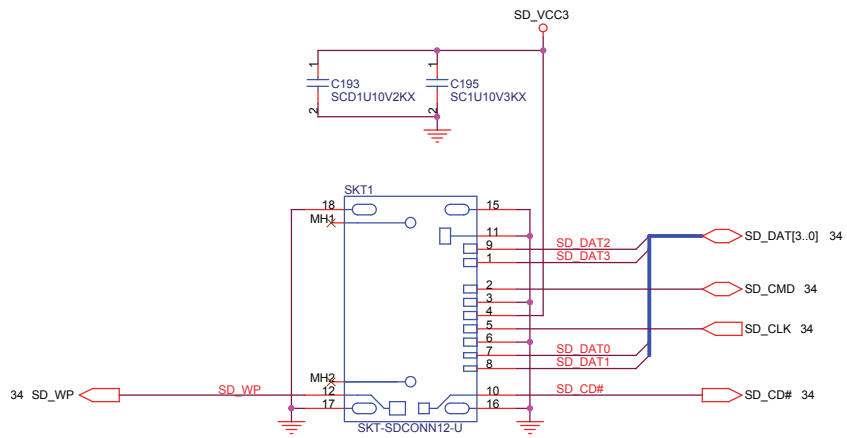
Title		
CARD BUS(2/2)		
Size	Document Number	Rev
A3	S Note-3	-1
Date:	Tuesday, June 21, 2005	Sheet 35 of 75



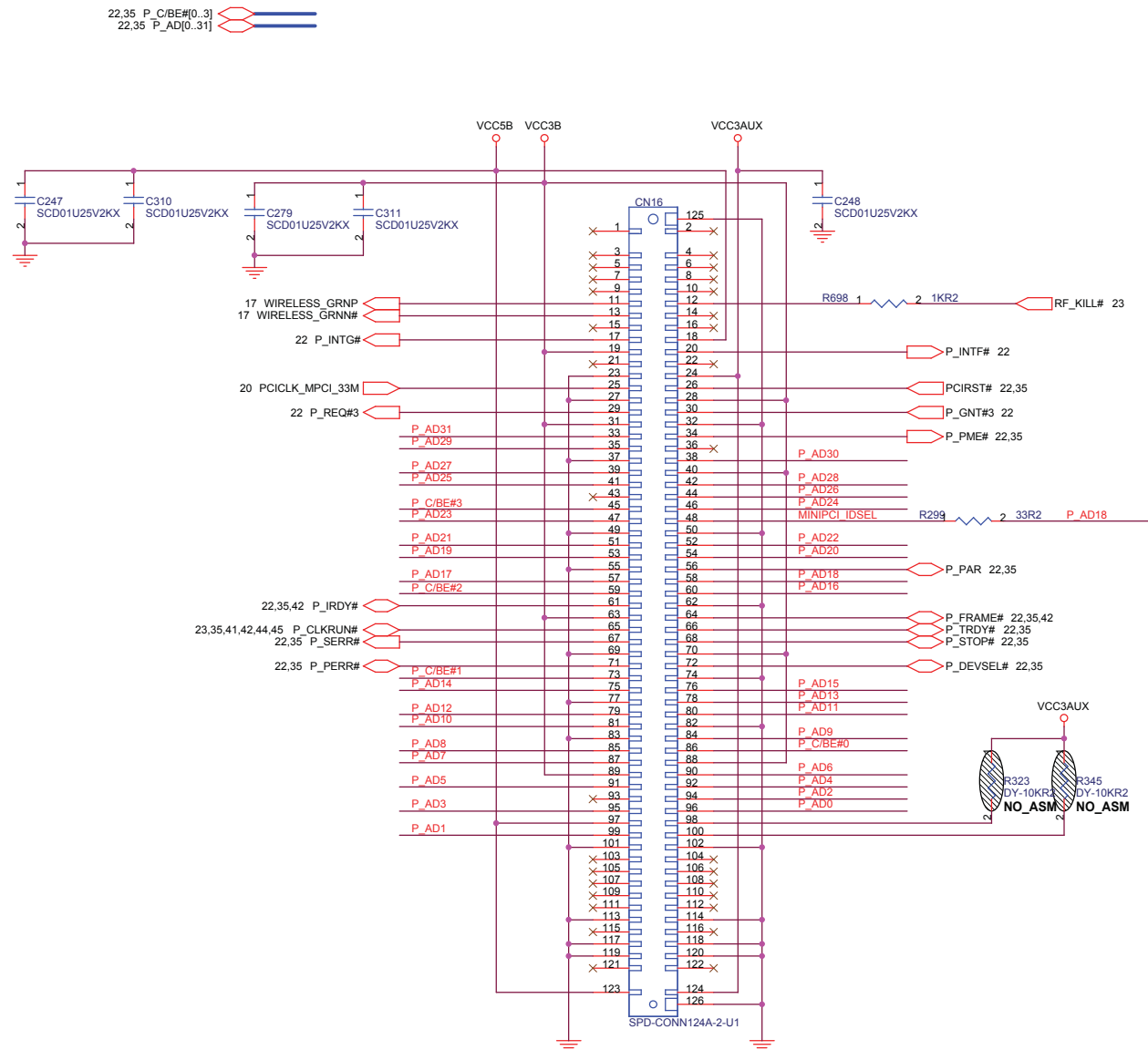


<http://hobi-elektronika.net>

***SD Slot***

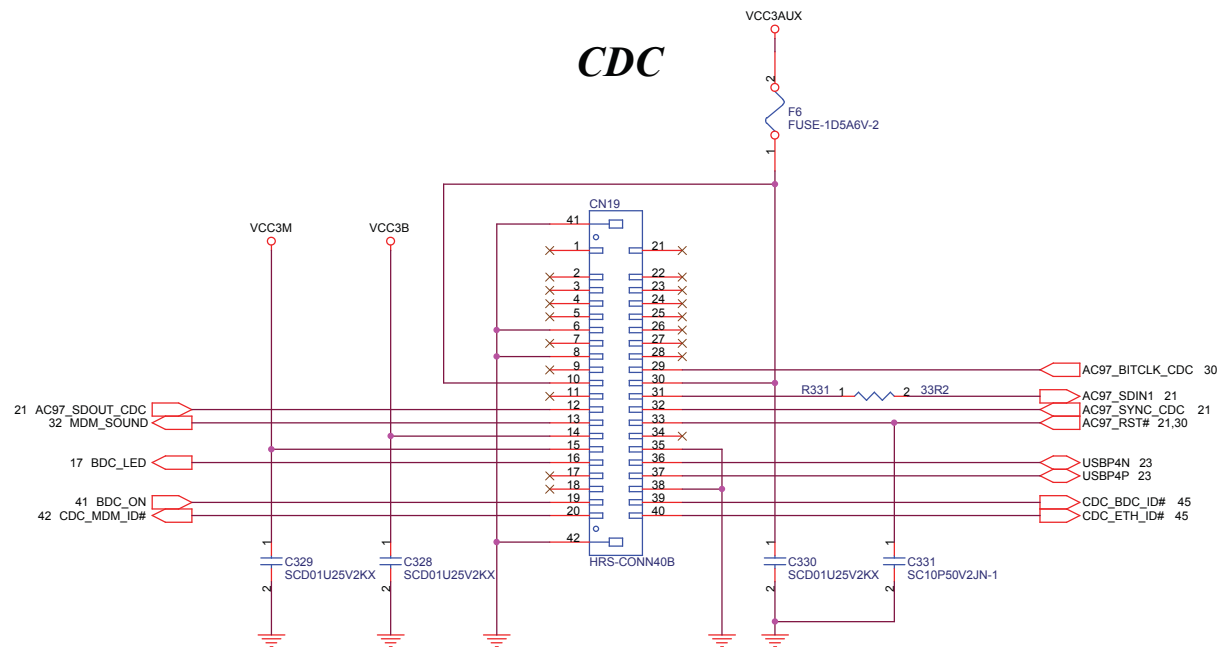


<http://hobi-elektronika.net>



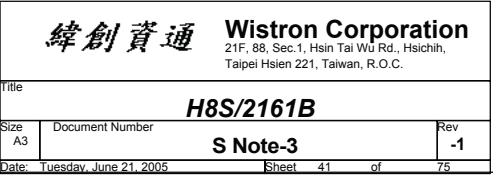
<http://hobi-elektronika.net>





<http://hobi-elektronika.net>

<b>緯創資通</b> Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	
<b>CDC/Fingerprint CONNECTOR</b>	
Size A3	Document Number <b>S Note-3</b>
Date: Tuesday, June 21, 2005	Rev -1
Sheet 40	of 75





FWH GPin	4	3	2	1	0	Base Memory	Memory	Memory Bank	
BASE_MEM_IDn	4	3	2	1	0	Total Capacity	Capacity	( Device Q'ty )	
	0	0	0	x	x	128MB	256Mbit	1 ( 4pcs )	Reserved
	0	0	1	x	x	256MB	256Mbit	2 ( 8pcs )	Reserved
	0	1	0	0	0	256MB	512Mbit	1 ( 4pcs )	Elpida / Hynix / Infineon / Micron / Samsung
				0	1				Reserved
				1	0				Reserved
				1	1				Reserved
	0	1	1	0	0	512MB	512Mbit	2 ( 8pcs )	Elpida / Hynix / Infineon / Micron / Samsung
				0	1				Reserved
				1	0				Reserved
				1	1				Reserved
	1	0	0	x	x	512MB	1Gbit	1 ( 4pcs )	Reserved
	1	0	1	x	x	1GB	1Gbit	2 ( 8pcs )	Reserved

Bit 4-3 : Memory Capacity ID[1:0]  
 00 = 256Mbit  
 01 = 512Mbit  
 10 = 1Gbit  
 11 = Reserved

Bit 2 : Memory Bank ID  
 0 = 1 Rank ( 4pcs )  
 1 = 2 Rank ( 8pcs )

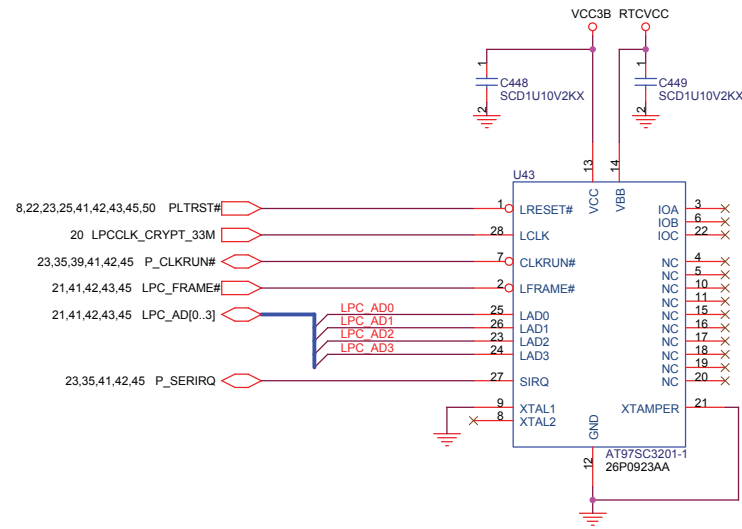
Bit 1-0 : Memory Vendor ID[1:0]  
 00 = Default  
 01 = Reserved ( for Specific Memory Vendor )  
 10 = Reserved ( for Specific Memory Vendor )  
 11 = Reserved ( for Specific Memory Vendor )

x : don't care  
 11000b ~ 11111b : reserved

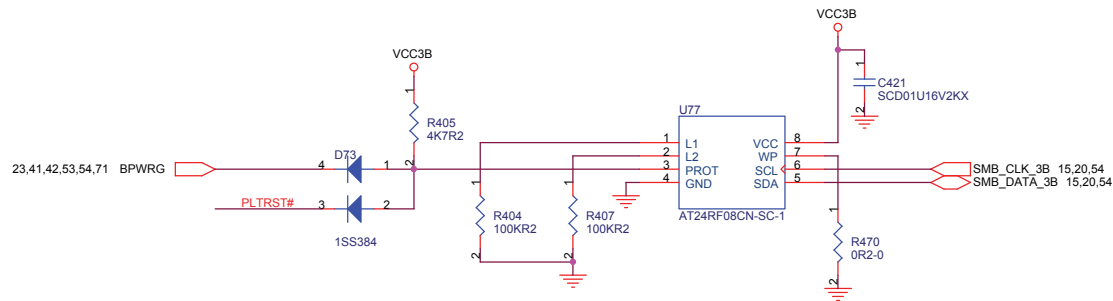


Title			
<b><i>FWH</i></b>			
Size A3	Document Number		Rev -1
<b>S Note-3</b>			
Date:	Tuesday, June 21, 2005	Sheet 43 of 75	

# TCPA



# EEPROM

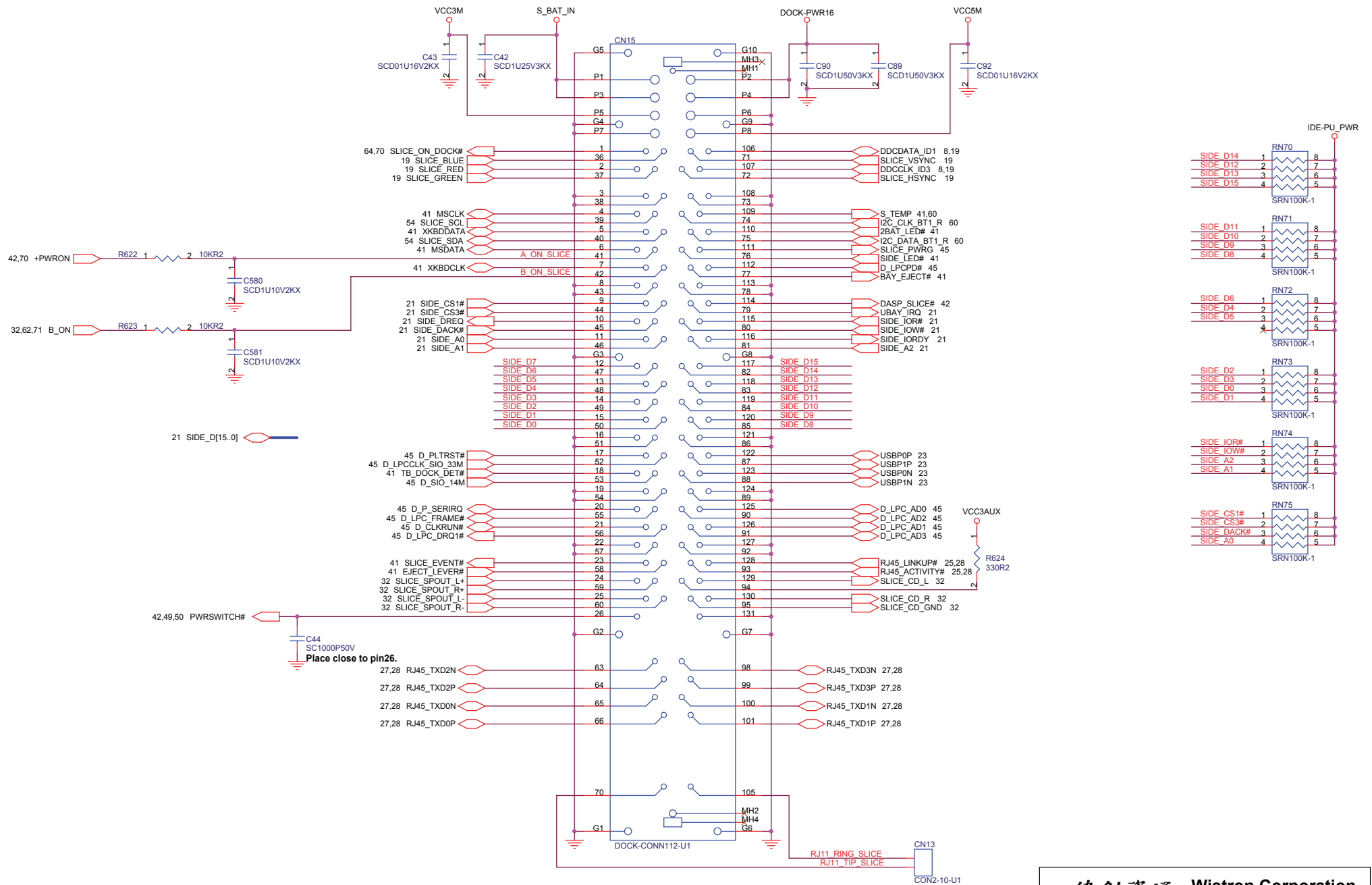


<http://hobi-elektronika.net>

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Taipei Hsien 221, Taiwan, R.O.C.

Title			<b>EEPROM / TPM</b>	
Size	Document Number		Rev	
A3			<b>S Note-3</b>	
Date: Tuesday, June 21, 2005		Sheet	44 of	75

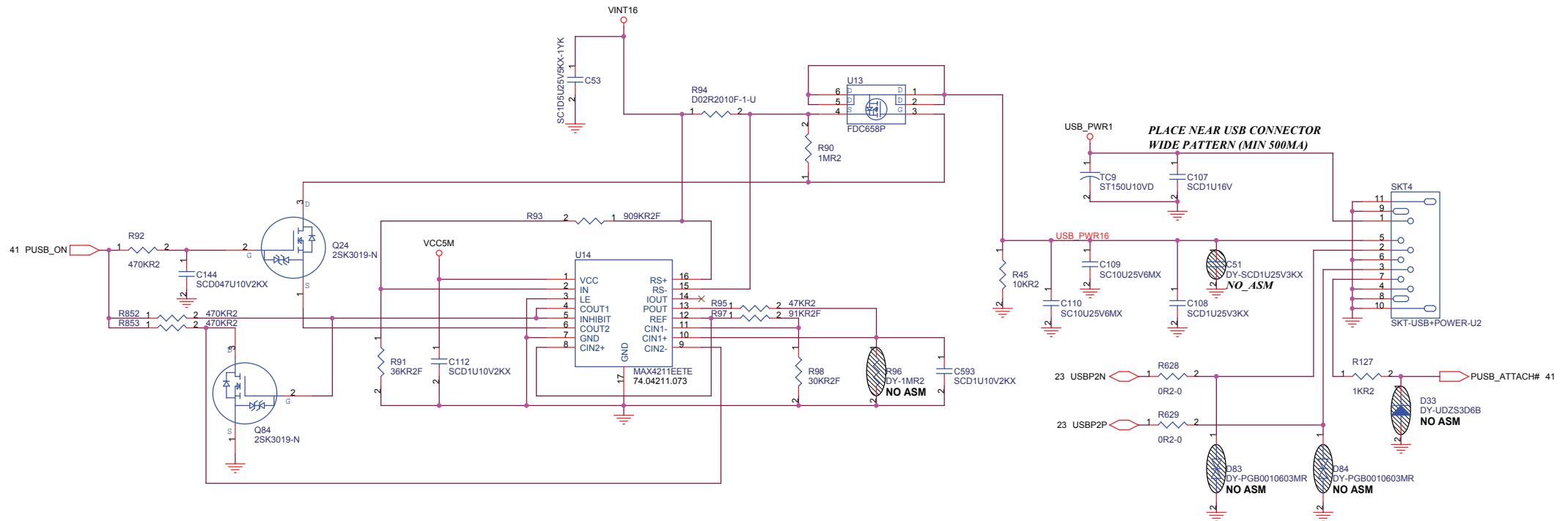




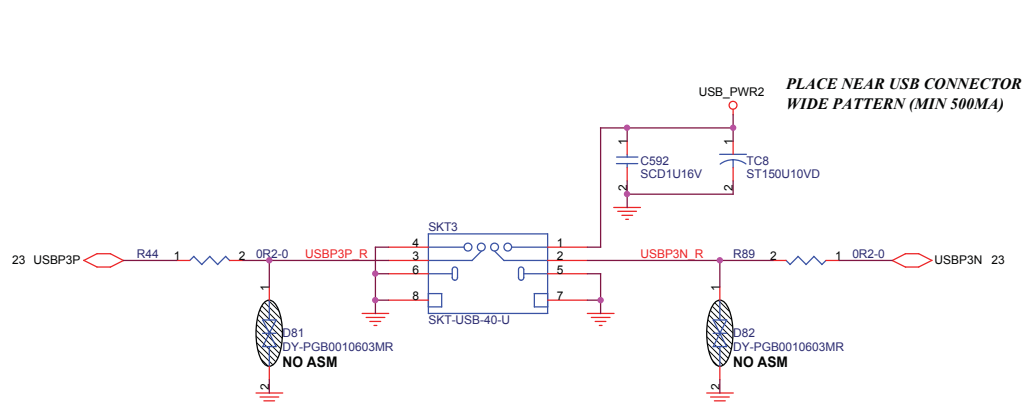
<http://hobi-elektronika.net>



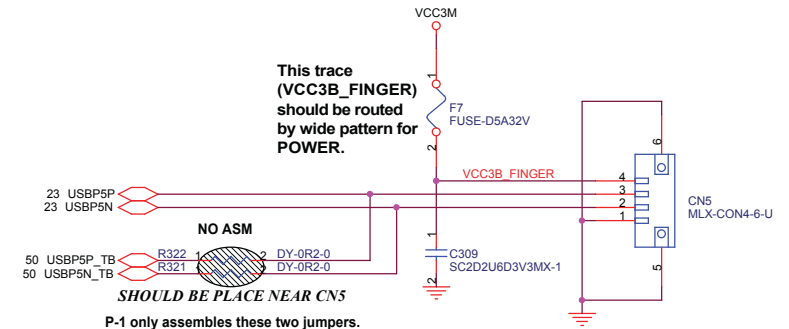
# Powered USB Connector

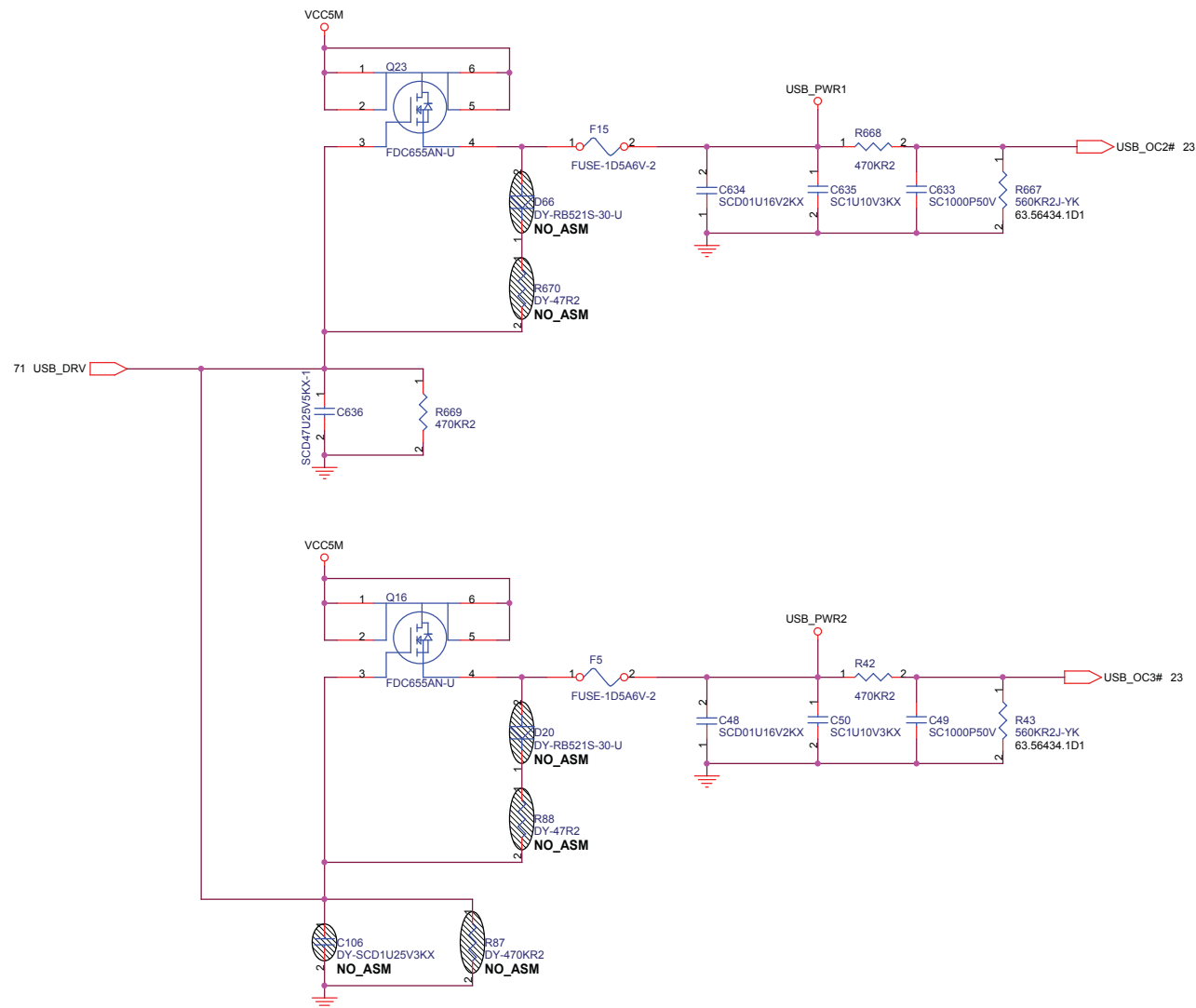


## USB Connector

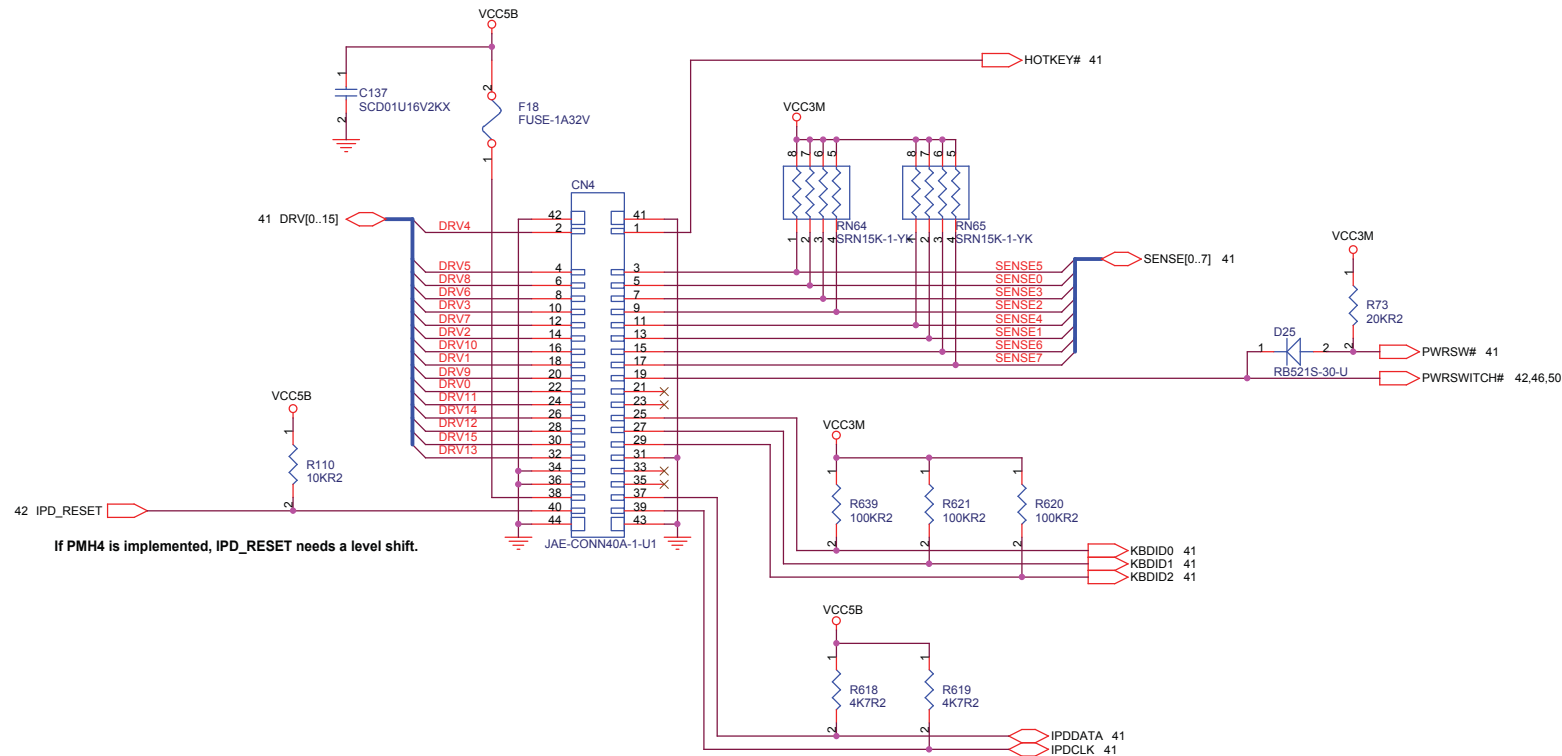


## Fingerprint

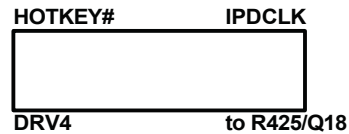




# Keyboard Connector



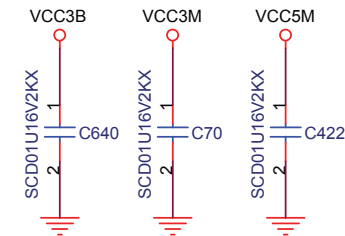
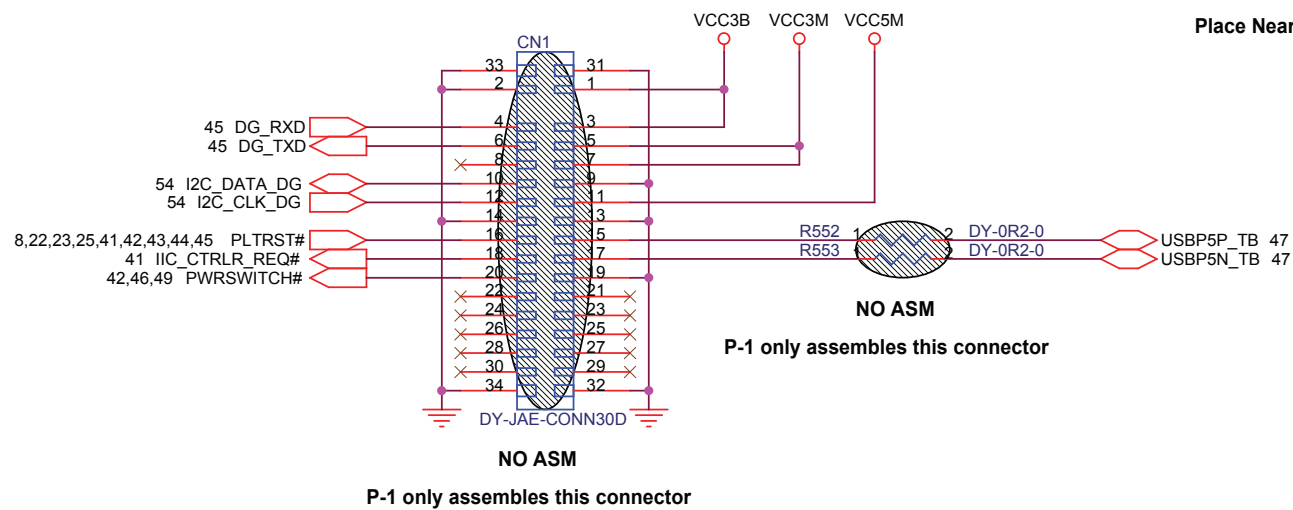
If PMH4 is implemented, IPD\_RESET needs a level shift.



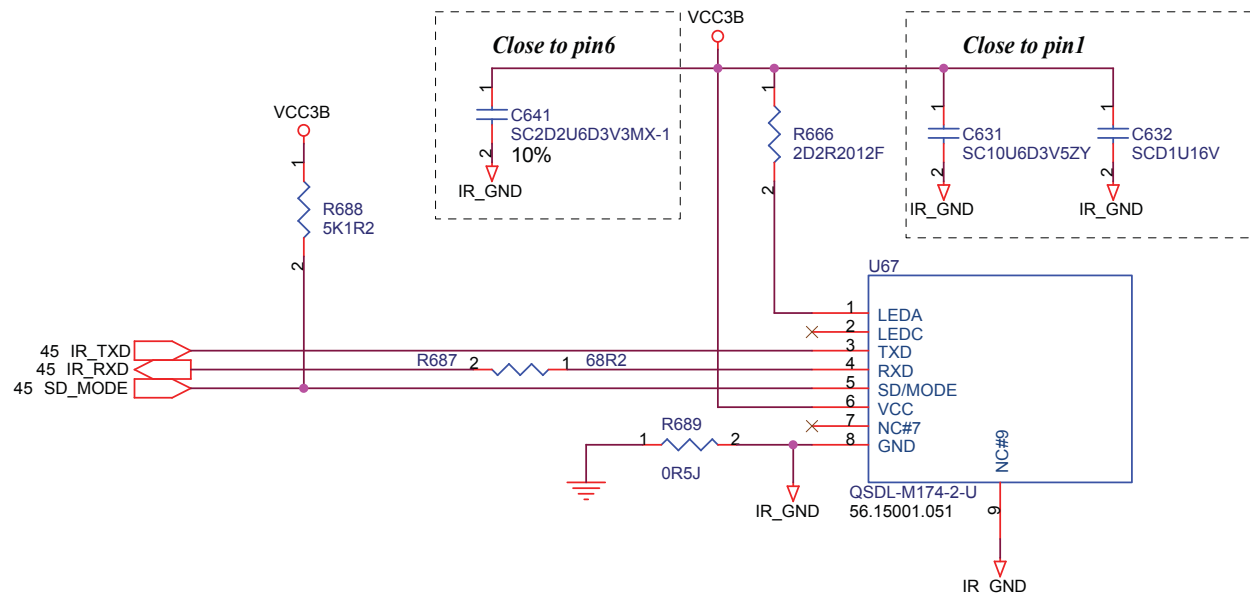
Keyboard Connector Top View

<http://hobi-elektronika.net>

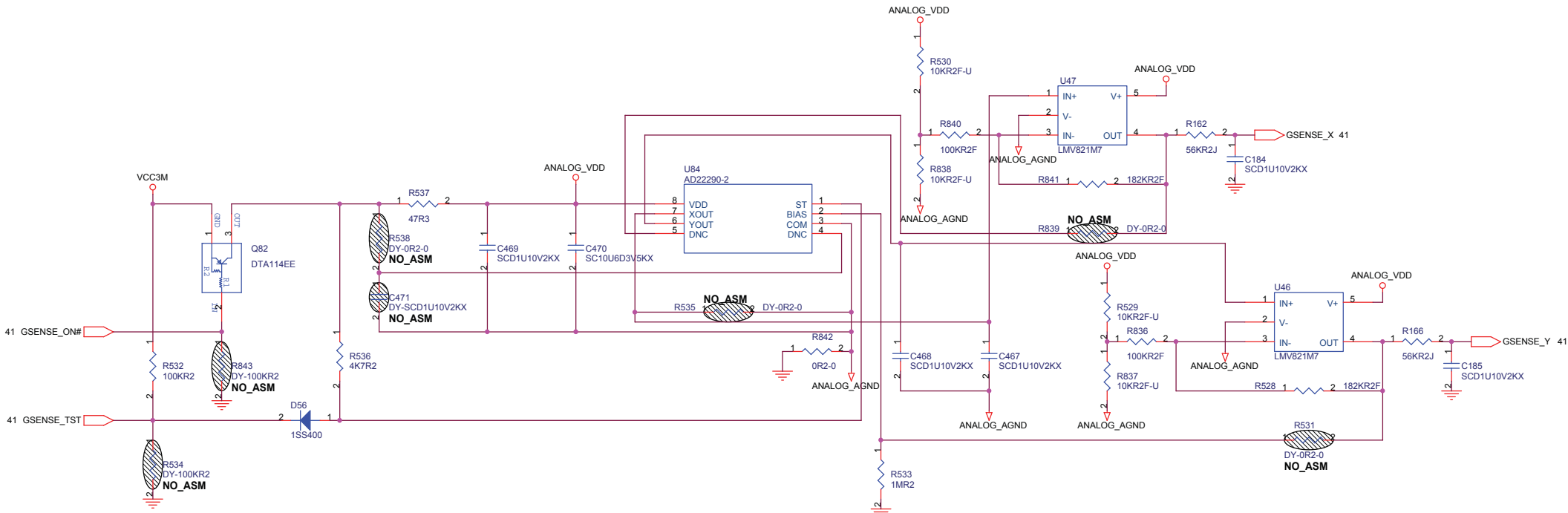
<b>緯創資通 Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	
<b>KEYBOARD CONN</b>	
Size A3	Document Number
<b>S Note-3</b>	
Date: Tuesday, June 21, 2005	Sheet 49 of 75
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Place Near to CONN



SHOULD QUALIFY BELOW PARTS  
 PRIMATY ALIGENT QSDL-M174-1 : 74.00174.001  
 SECOND VISHAY TFDU6102 : 56.15001.051



### Accelerometer Meter Parts List

Page	Usage Code	AD22290	MEMSIC	NO ACC.
52	R843 (100K 5%)	NO_ASM	NO_ASM	ASM
52	R534 (100K 5%)	NO_ASM	ASM	ASM
52	U47 (MAX4400AXK-T-1)	ASM	NO_ASM	NO_ASM
52	U84 (AD22290-2)	ASM	MXA2500GL	NO_ASM
52	U46 (MAX4400AXK-T-1)	ASM	NO_ASM	NO_ASM
52	Q82 (DTA114EE)	ASM	ASM	NO_ASM
52	D56 (1SS400)	ASM	NO_ASM	NO_ASM
52	R537(47ohm 5%)	47 ohm	10 ohm	NO_ASM
52	R530(10K 1%)	ASM	NO_ASM	NO_ASM
52	R840(100K 1%)	ASM	NO_ASM	NO_ASM
52	R612(56K 5%)	ASM	8.2K 5%	NO_ASM
52	R838(10K 1%)	ASM	NO_ASM	NO_ASM
52	R841(182K 1%)	ASM	NO_ASM	NO_ASM
52	R536(4.7K 5%)	ASM	NO_ASM	NO_ASM
52	R529(10K 1%)	ASM	NO_ASM	NO_ASM
52	R836(100K 1%)	ASM	NO_ASM	NO_ASM
52	R116(56K 5%)	ASM	8.2K 5%	NO_ASM
52	R842(0ohm 5%)	ASM	ASM	NO_ASM

Page	Usage Code	AD22290	MEMSIC	NO ACC.
52	R532(100K 5%)	ASM	NO_ASM	NO_ASM
52	R837(10K 1%)	ASM	NO_ASM	NO_ASM
52	R528(182K 1%)	ASM	NO_ASM	NO_ASM
52	R533(1M 5%)	ASM	NO_ASM	NO_ASM
52	C184 (0.1u 10%10V)	ASM	ASM	NO_ASM
52	C469 (0.1u 10%10V)	ASM	ASM	NO_ASM
52	C468 (0.1u 10%10V)	ASM	NO_ASM	NO_ASM
52	C467 (0.1u 10%10V)	ASM	NO_ASM	NO_ASM
52	C185 (0.1u 10%10V)	ASM	ASM	NO_ASM
52	R538(0ohm 5%)	NO_ASM	ASM	NO_ASM
52	R839(0ohm 5%)	NO_ASM	ASM	NO_ASM
52	R535(0ohm 5%)	NO_ASM	ASM	NO_ASM
52	R531(0ohm 5%)	NO_ASM	ASM	NO_ASM
52	C741 (0.1u 10%10V)	NO_ASM	ASM	NO_ASM
52	C470 (10u 10% 6.3V)	ASM	NO_ASM	NO_ASM

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title

G-SENSOR

Size

A3

Document Number

S Note-3

Date:

Tuesday, June 21, 2005

Sheet

52

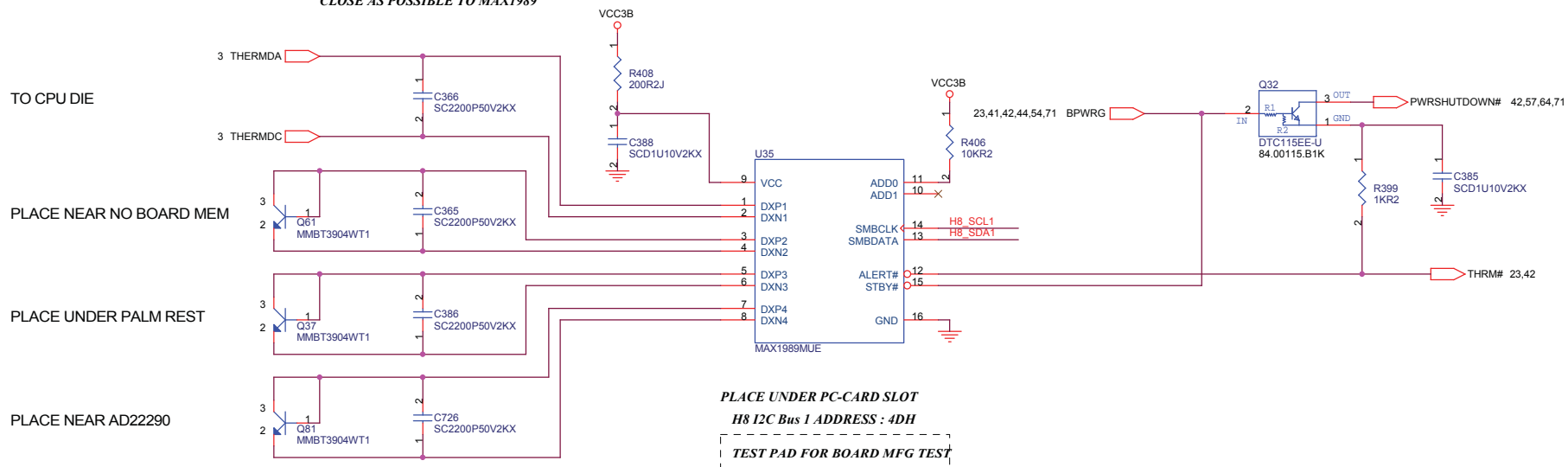
of

75

Rev

-1

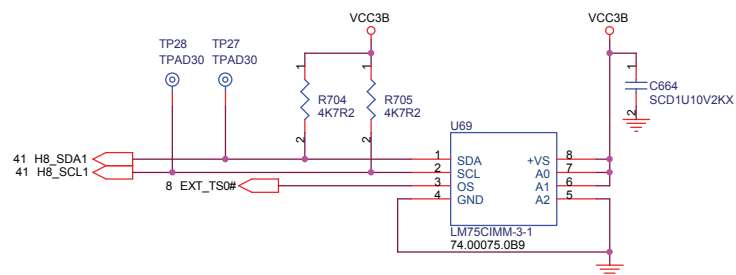
THESE CAPS MUST BE PLACED AS  
CLOSE AS POSSIBLE TO MAX1989



## Thermal Sensor LM75 for DDR module

(For DDR throttling implementation)

PLACE UNDER DIMM



H8 I2C Bus 1 ADDRESS : 4BH

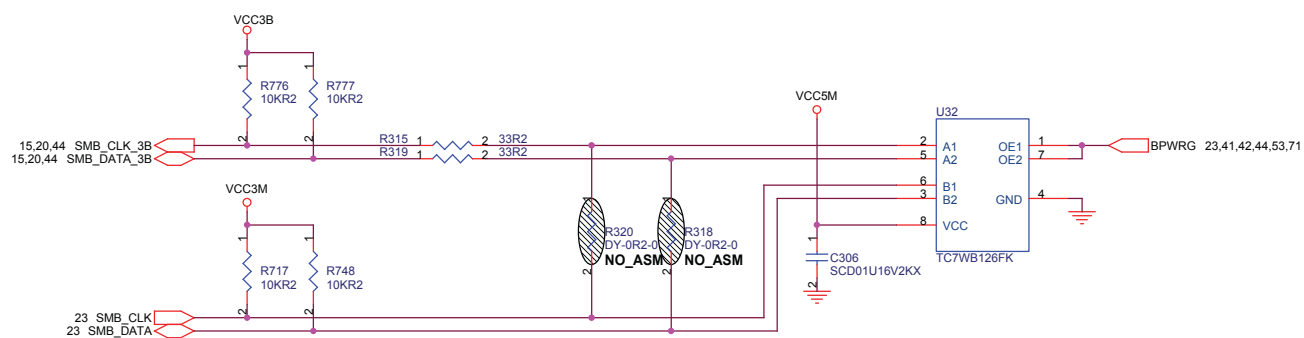
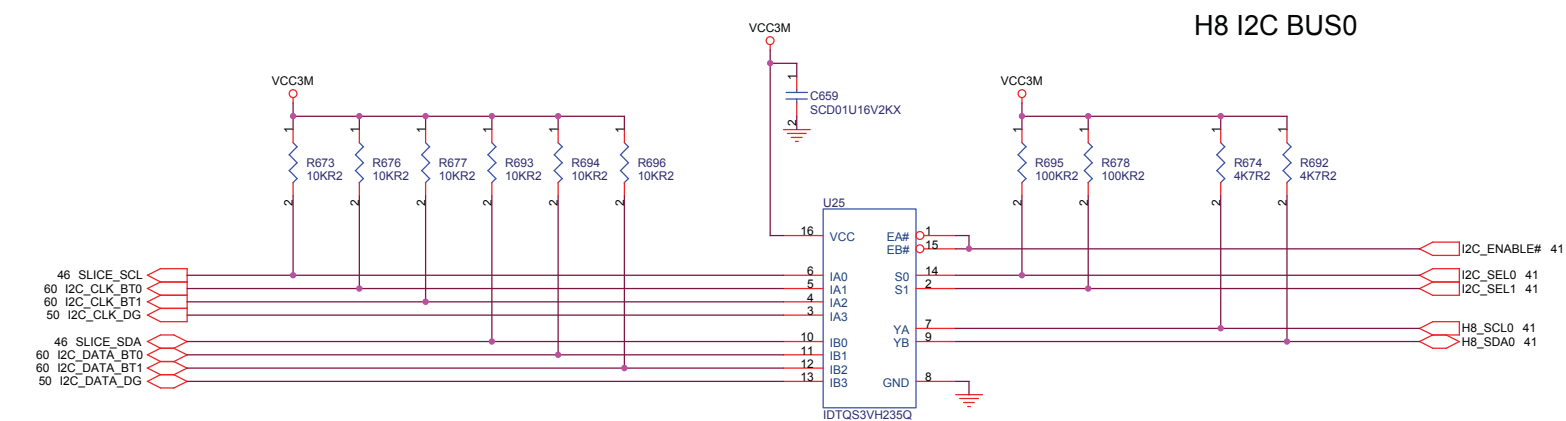
Main Source : 74.00075.0B9 (NS)  
2nd Source : 74.00075.AB9 (MAXIM)  
74.07416.0B9 (ADI)

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緯創資通 Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title			THERMAL SENSOR
Size	Document Number	Rev	
A3		-1	
Date:	Tuesday June 21, 2005	Sheet	53 of 75

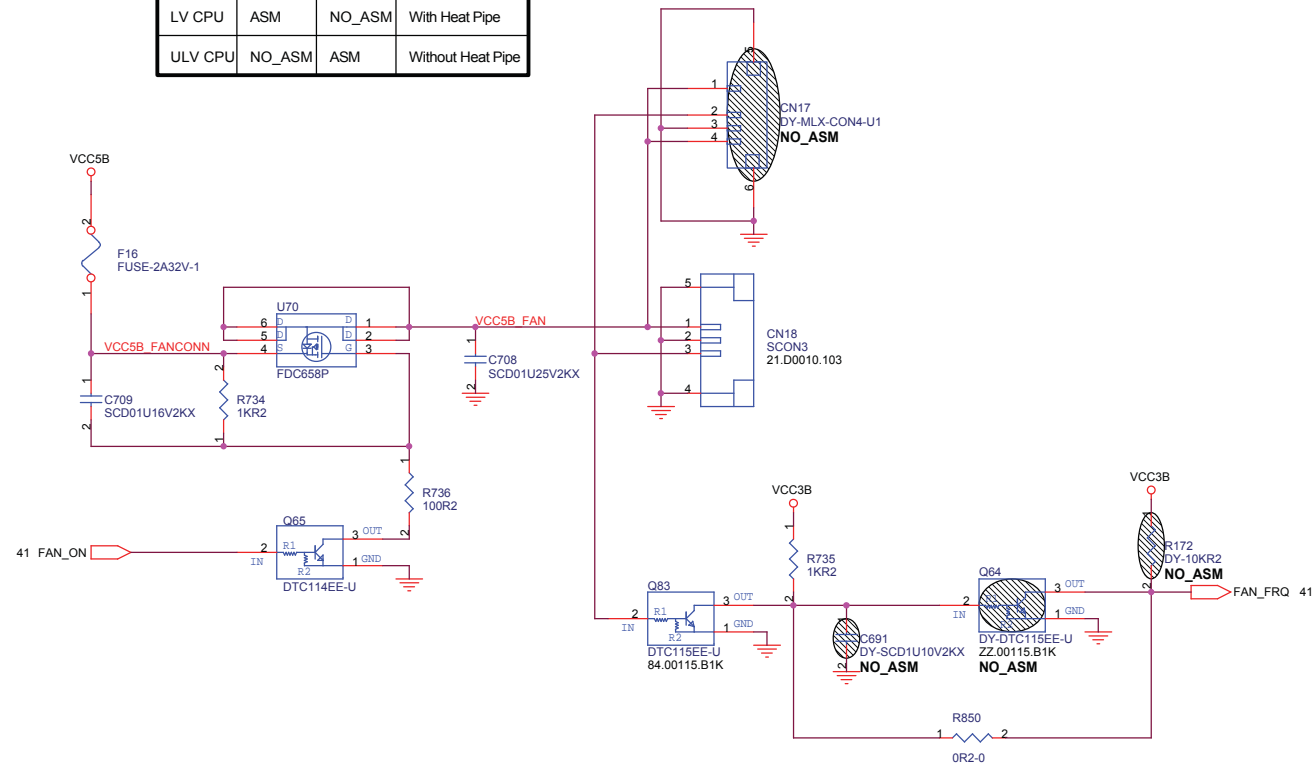




<http://hobi-elektronika.net>

## Fan Power Control

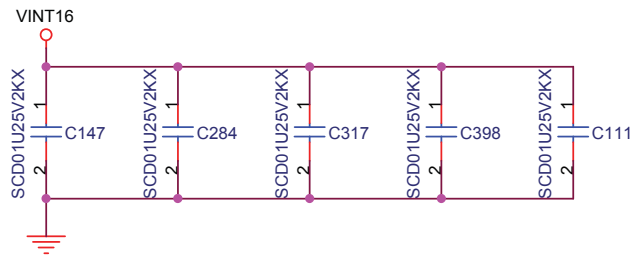
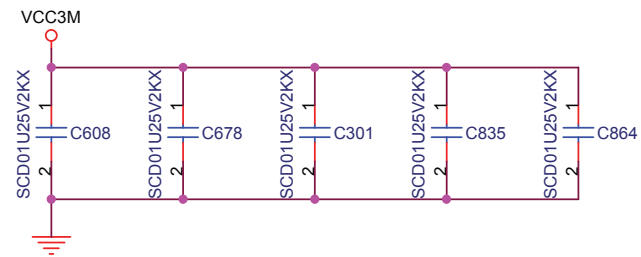
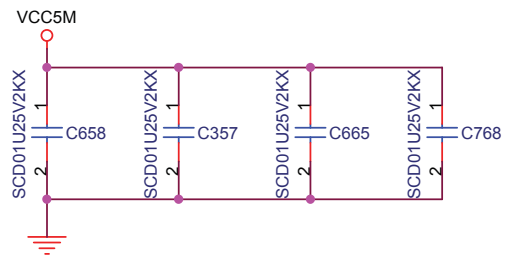
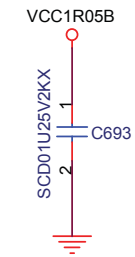
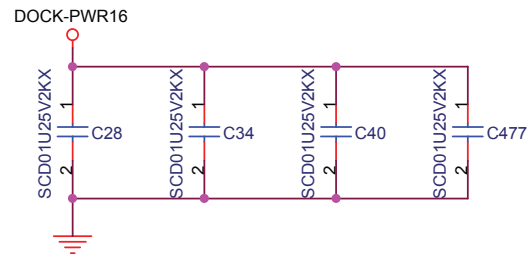
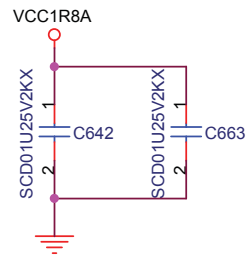
	CN18	CN17	FAN Module
LV CPU	ASM	NO_ASM	With Heat Pipe
ULV CPU	NO_ASM	ASM	Without Heat Pipe



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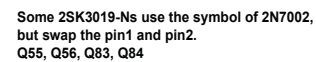
Title			<b>FAN CONTROL</b>	
Size	Document Number		Rev	
A3			<b>S Note-3</b>	
Date: Tuesday, June 21, 2005		Sheet	55	of 75

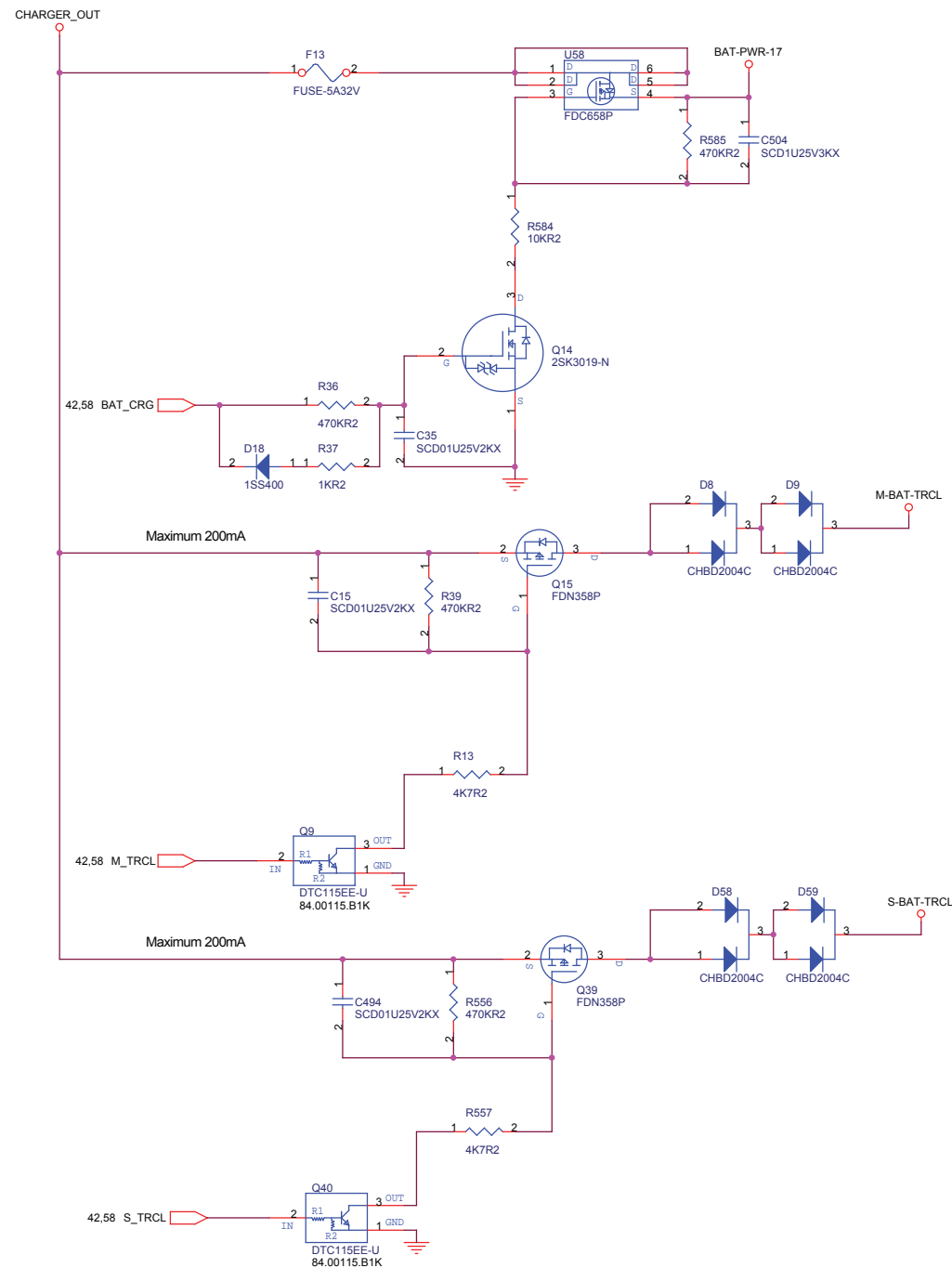


<http://hobi-elektronika.net>

<div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>	
Title	
EMC DECOUPLING	
Size A4	<div>Document Number</div> <div>S Note-3</div>
	<div>Rev</div> <div>-1</div>
Date: Tuesday, June 21, 2005	Sheet 56 of 75

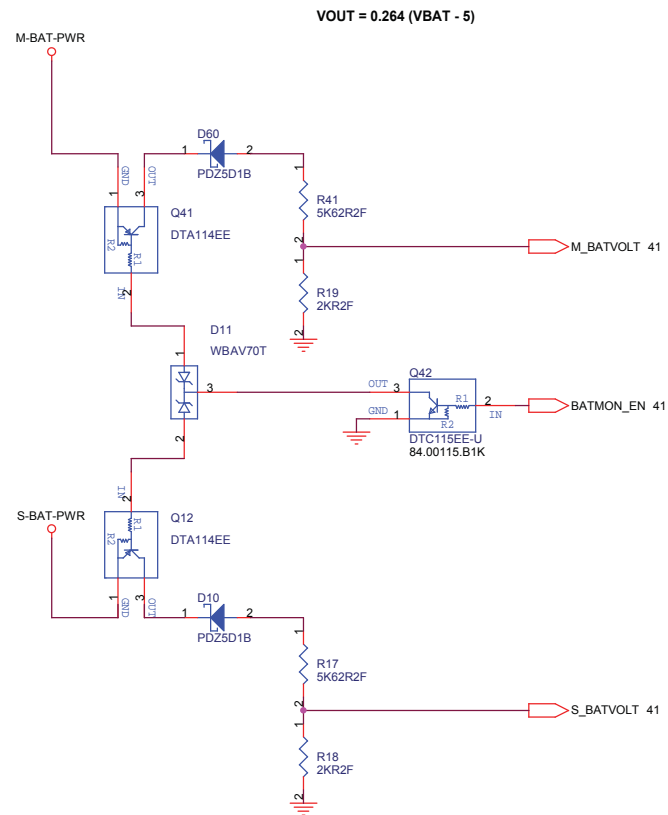






<http://hobi-elektronika.net>

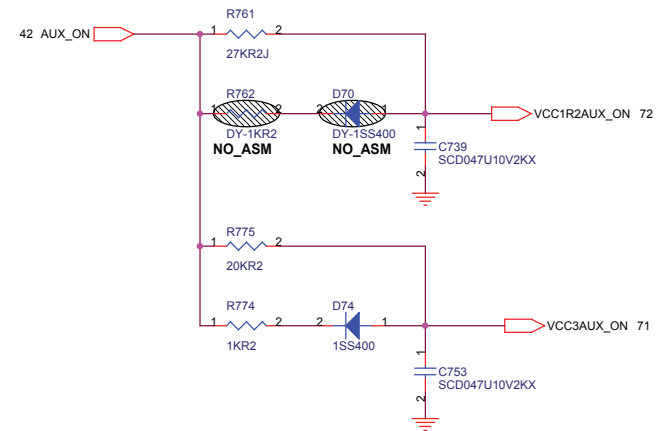
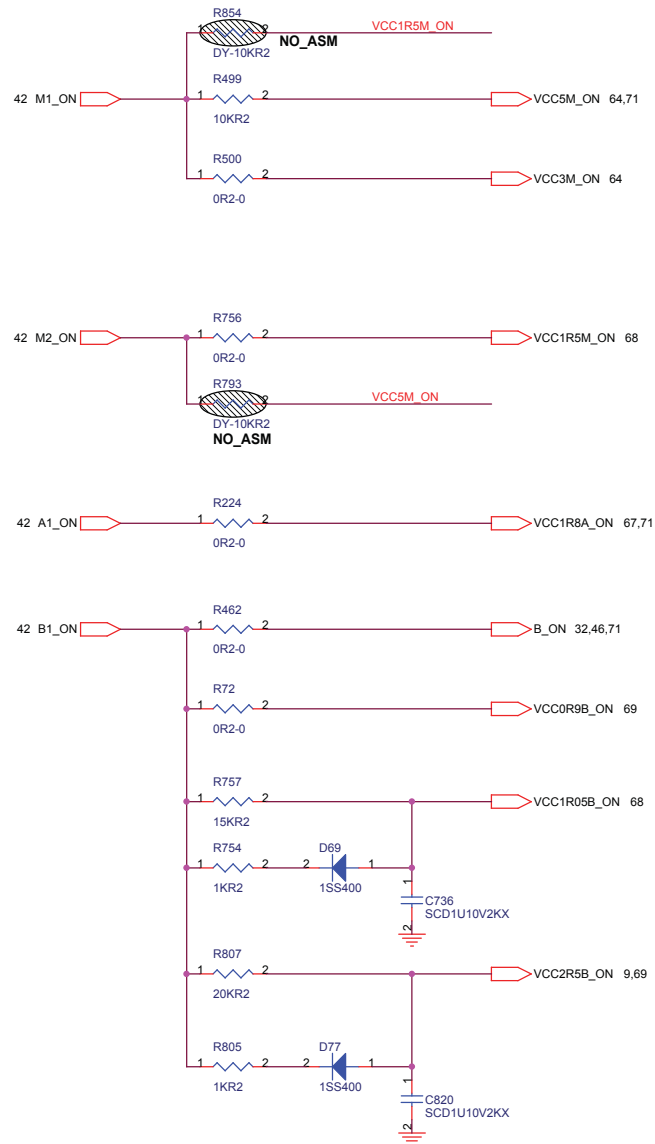




<http://hobi-elektronika.net>

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Title	
<b>BATTERY MONITOR</b>	
Size A3	Document Number <b>S Note-3</b>
Date: Tuesday, June 21, 2005	Sheet 61 of 75
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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title
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## DC-DC VDD15

Size	A4
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Document Number
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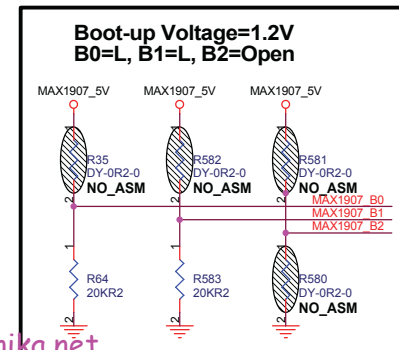
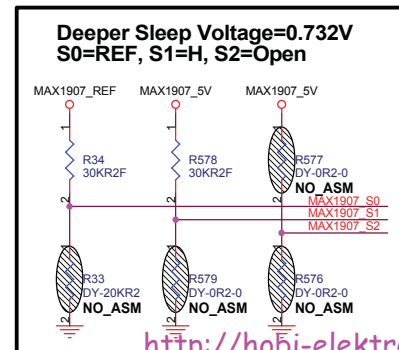
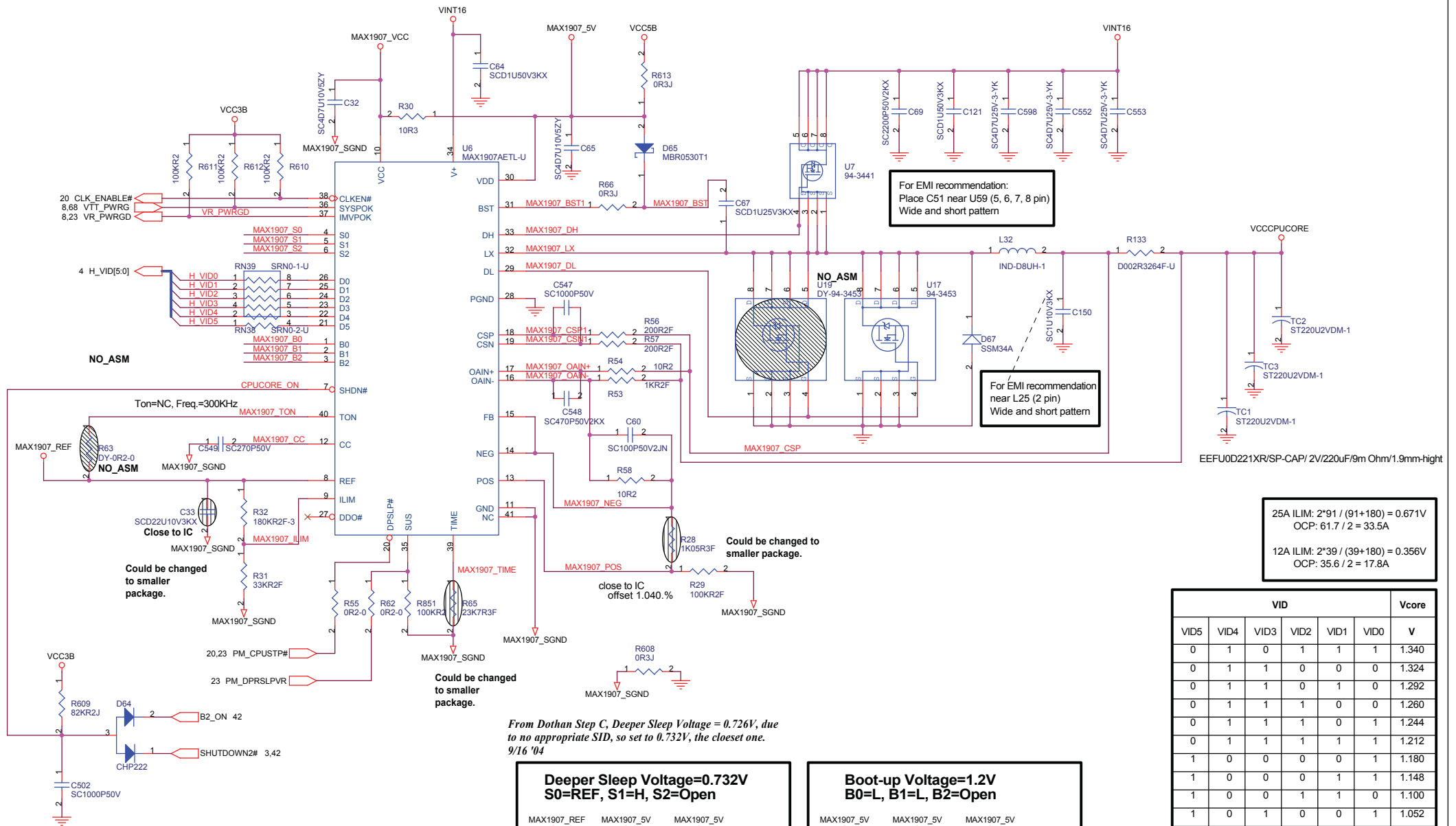
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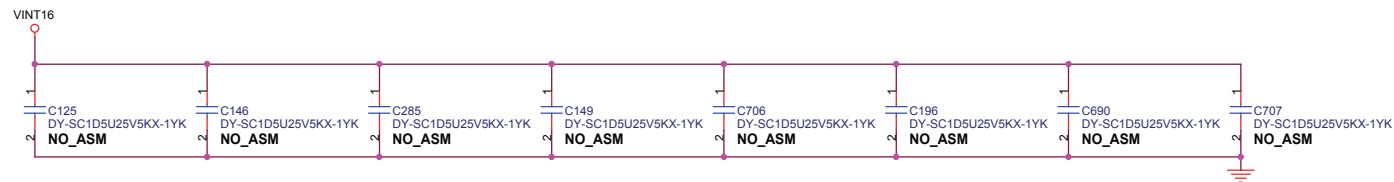


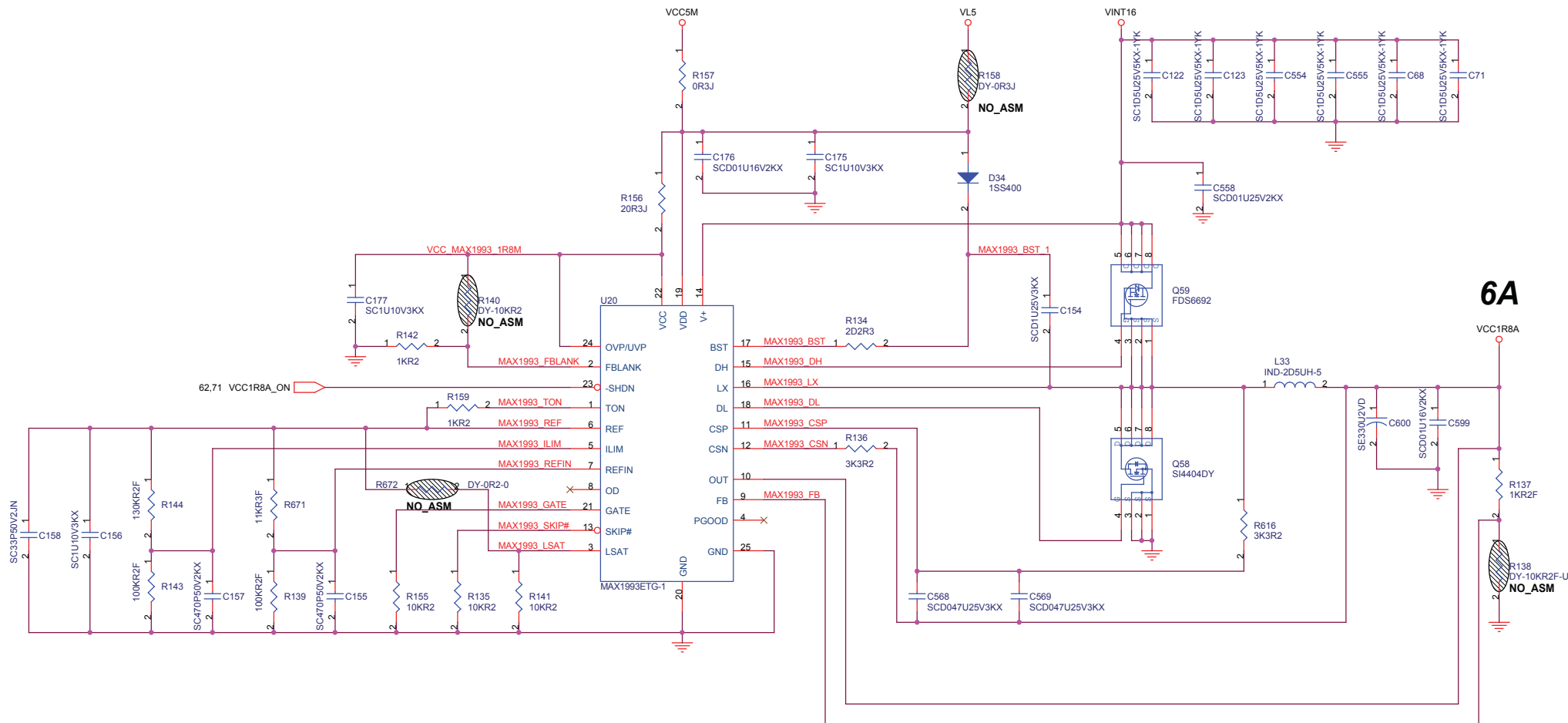
VID						Vcore
VID5	VID4	VID3	VID2	VID1	VID0	V
0	1	0	1	1	1	1.340
0	1	1	0	0	0	1.324
0	1	1	0	1	0	1.292
0	1	1	1	0	0	1.260
0	1	1	1	1	0	1.244
0	1	1	1	1	1	1.212
1	0	0	0	0	1	1.180
1	0	0	0	1	1	1.148
1	0	0	1	1	0	1.100
1	0	1	0	0	1	1.052
1	0	1	0	1	1	1.020
1	0	1	1	1	0	0.972
1	1	0	0	0	0	0.940

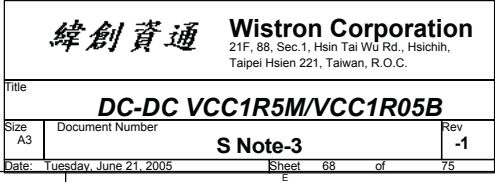
**緯創資通 Wistron Corporation**  
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Taipei Hsien 221, Taiwan, R.O.C.

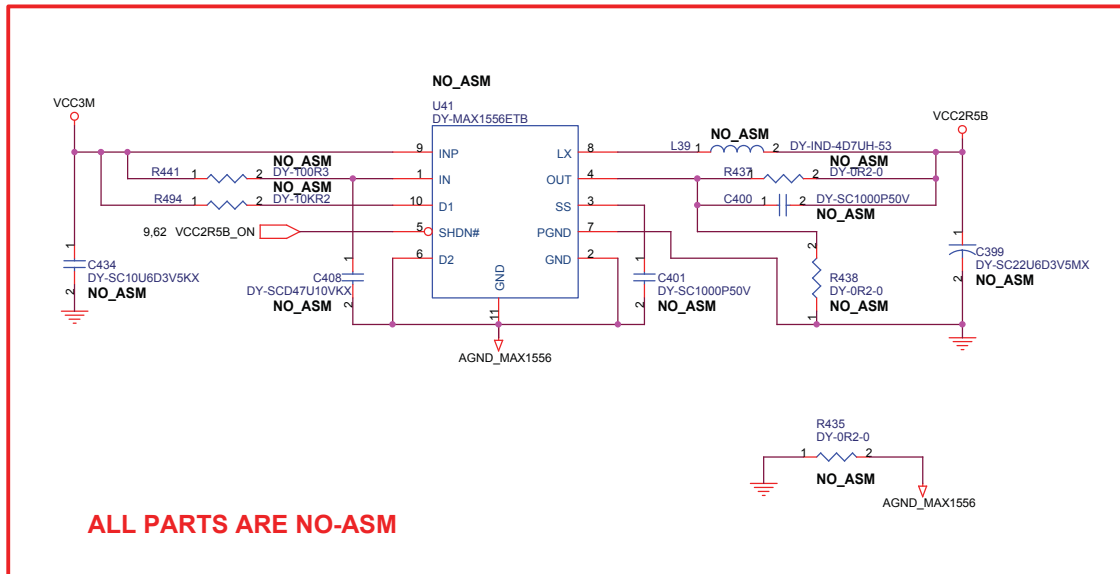
**ALL CAPS ON THIS PAGE ARE NO-ASM FOR VCCCPUCORE.**

## X8 PCS

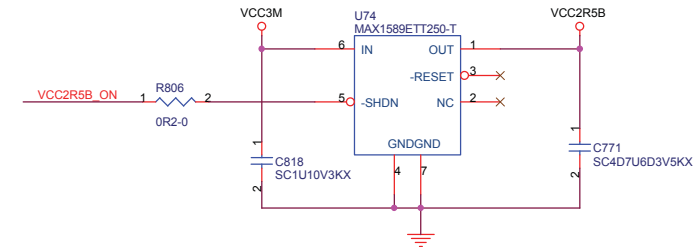




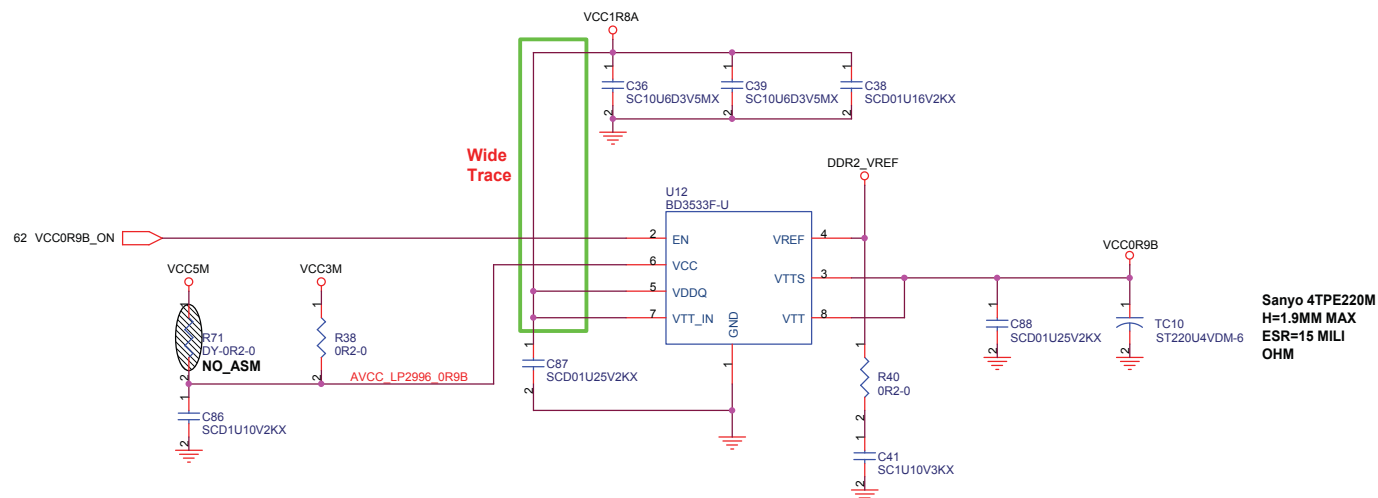




## VCC2R5B



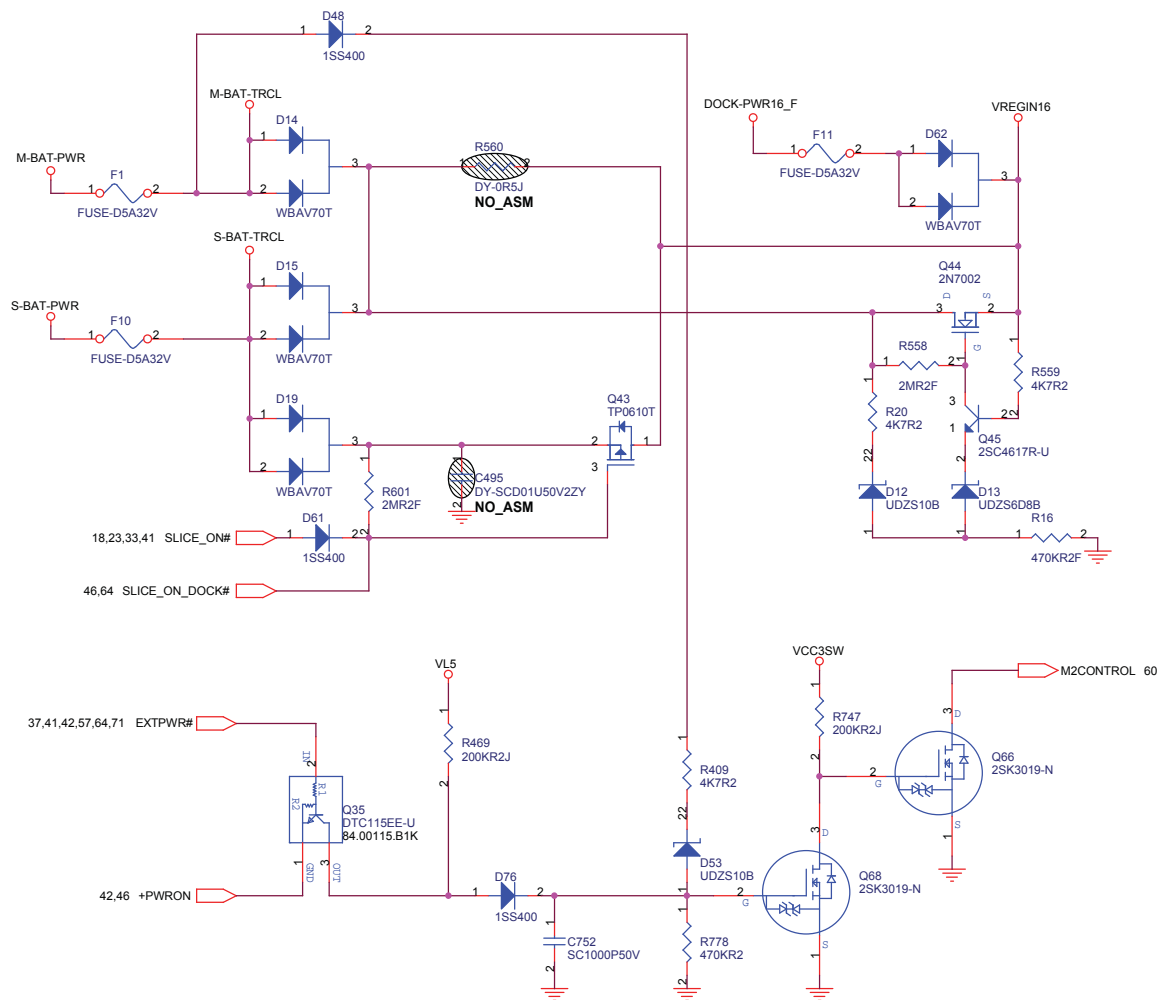
## VCC0R9B & DDR2\_VREF



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<b>緯創資通 Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	
DC-DC VCC2R5B/VCC0R9B	
Size	Document Number
A3	S Note-3
Date:	Rev
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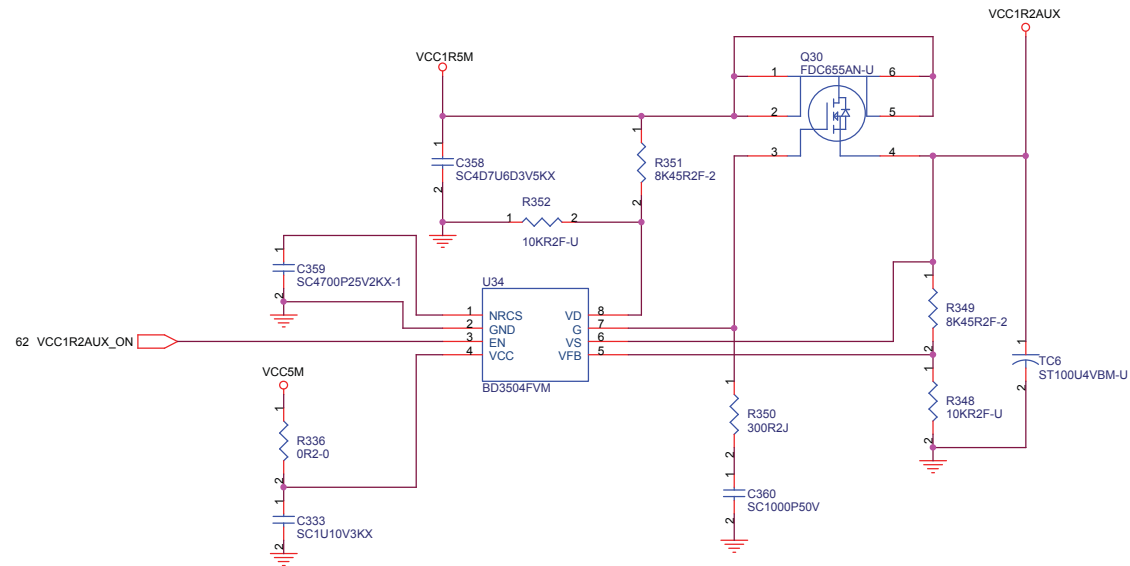




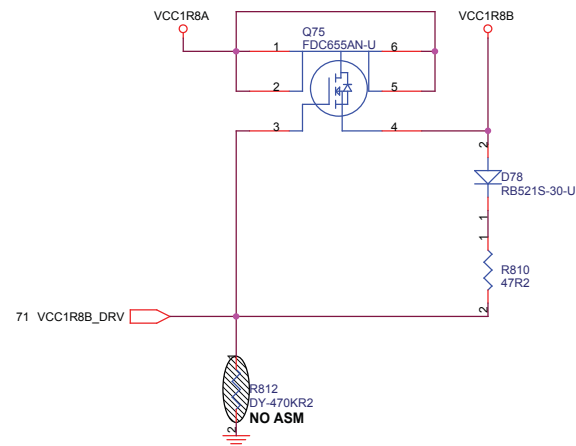
<http://hobi-elektronika.net>



## VCC1R2AUX



## VCC1R8B



## VCC3AUX

